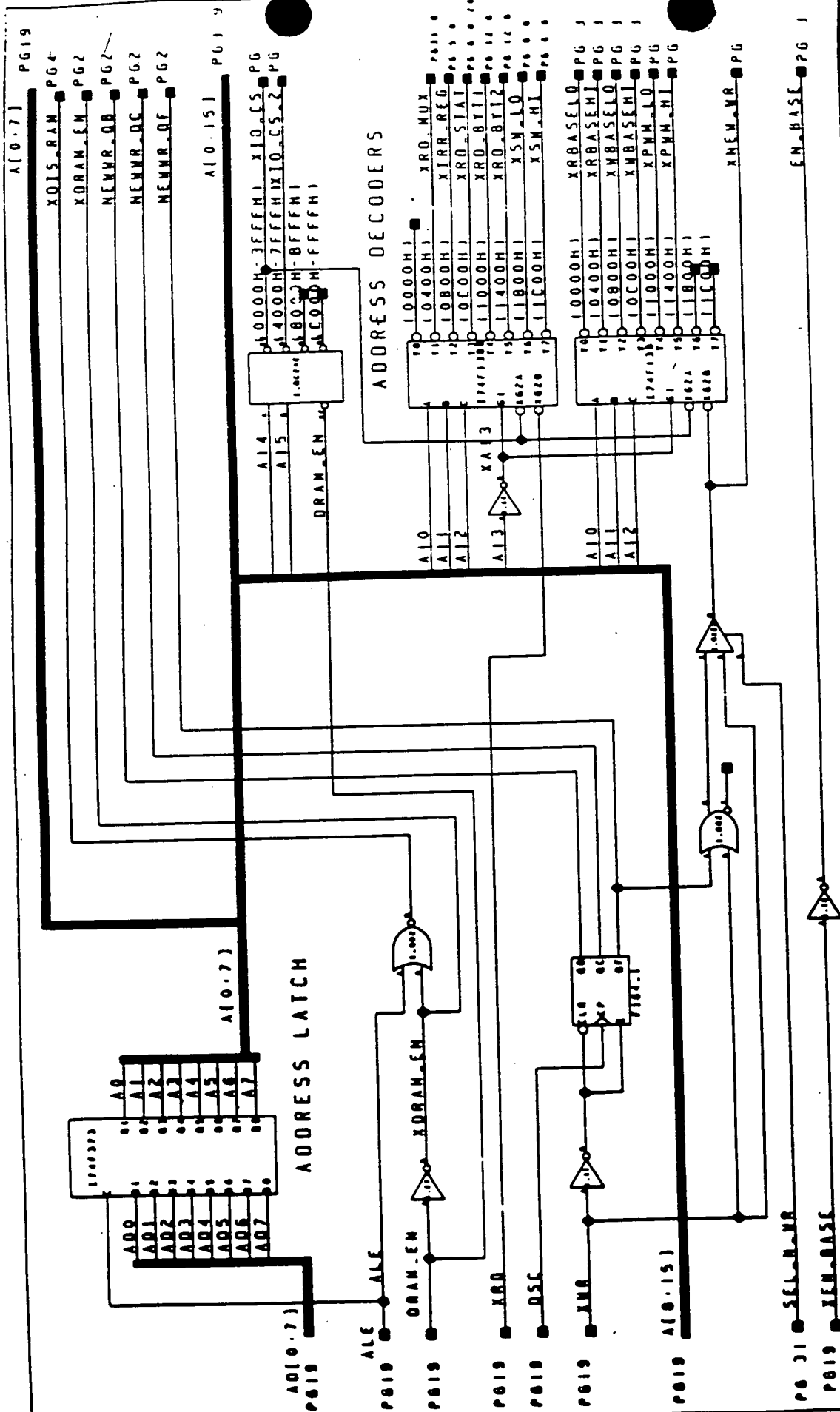


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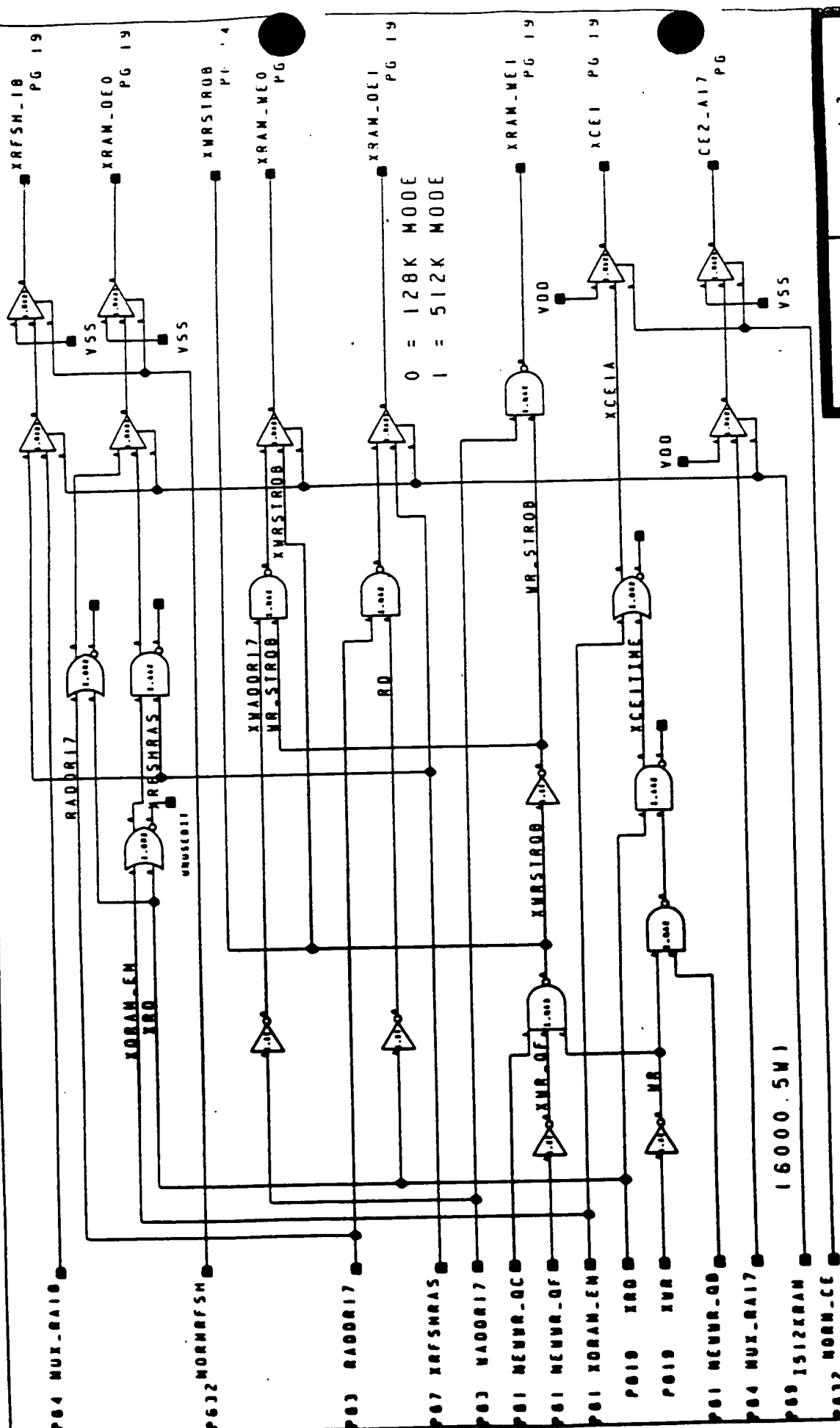
SYSTEM AND METHOD FOR TRANSMITTING AND UTILIZING
ELECTRONIC PROGRAMS GUIDE INFORMATION

APPENDIX A

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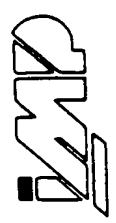


SCHEMATIC. DBE 1200
P/N 10-01-00003

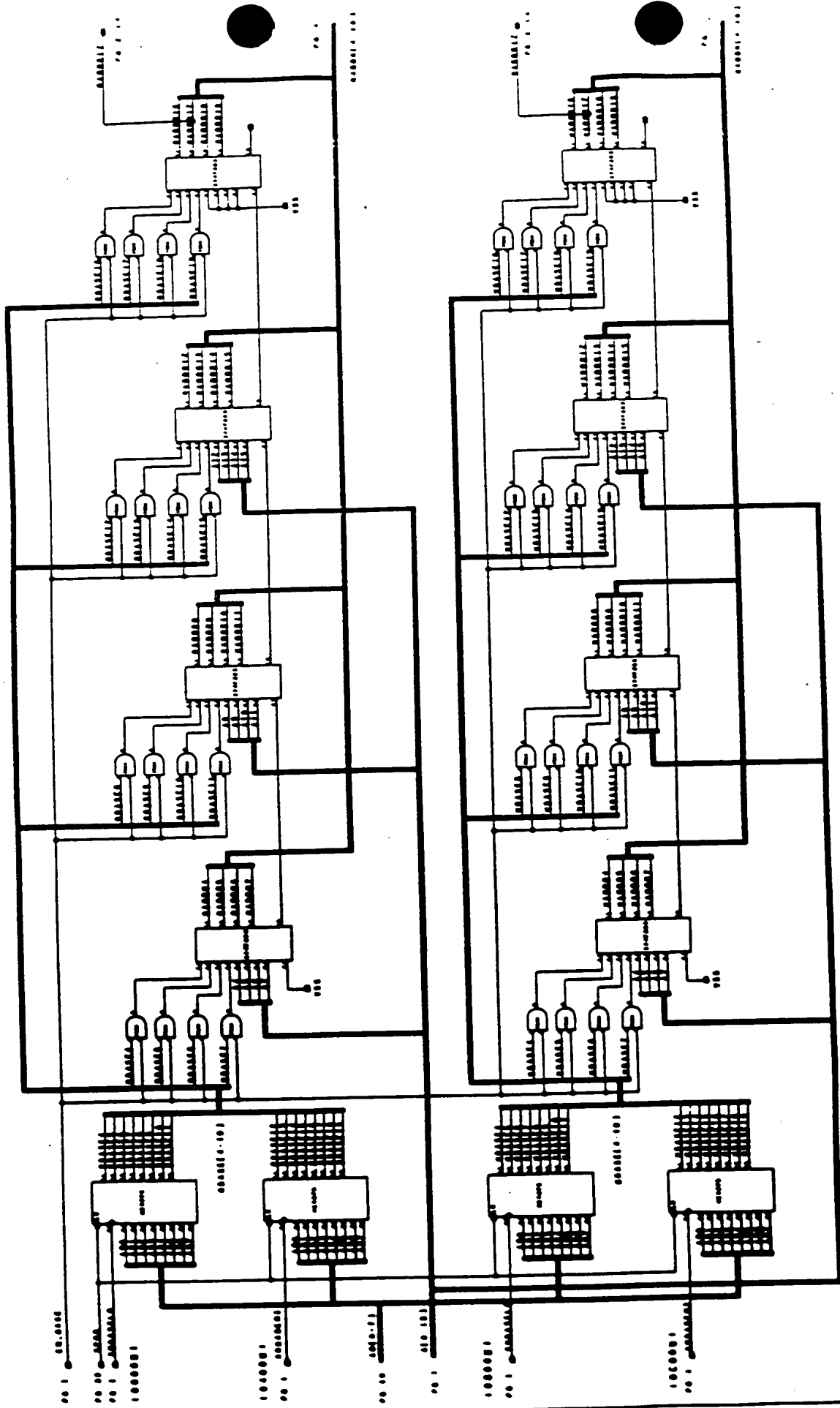


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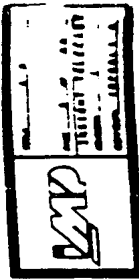
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PG3 RADDR[4:19]

PG3 WADDR[4:19]

VDD

PG19 XWR

PG33 XPOR
PG1 XOIS_RAM
PG7 XALE

PSRAM ADDRESS MUX

RADDR19 MUX_RA19 PG31.14
WADDR19

RADDR18 MUX_RA18 PG2.14
WADDR18

RADDR17 MUX_RA17 PG2.14
WADDR17

RADDR16 MUX_RA16 PG19.14
WADDR16

RADDR15 MUX_RA15 PG19.14
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RADDR13 MUX_RA13 PG19.14
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RADDR12 MUX_RA12 PG19.14
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RADDR11 MUX_RA11 PG19.14
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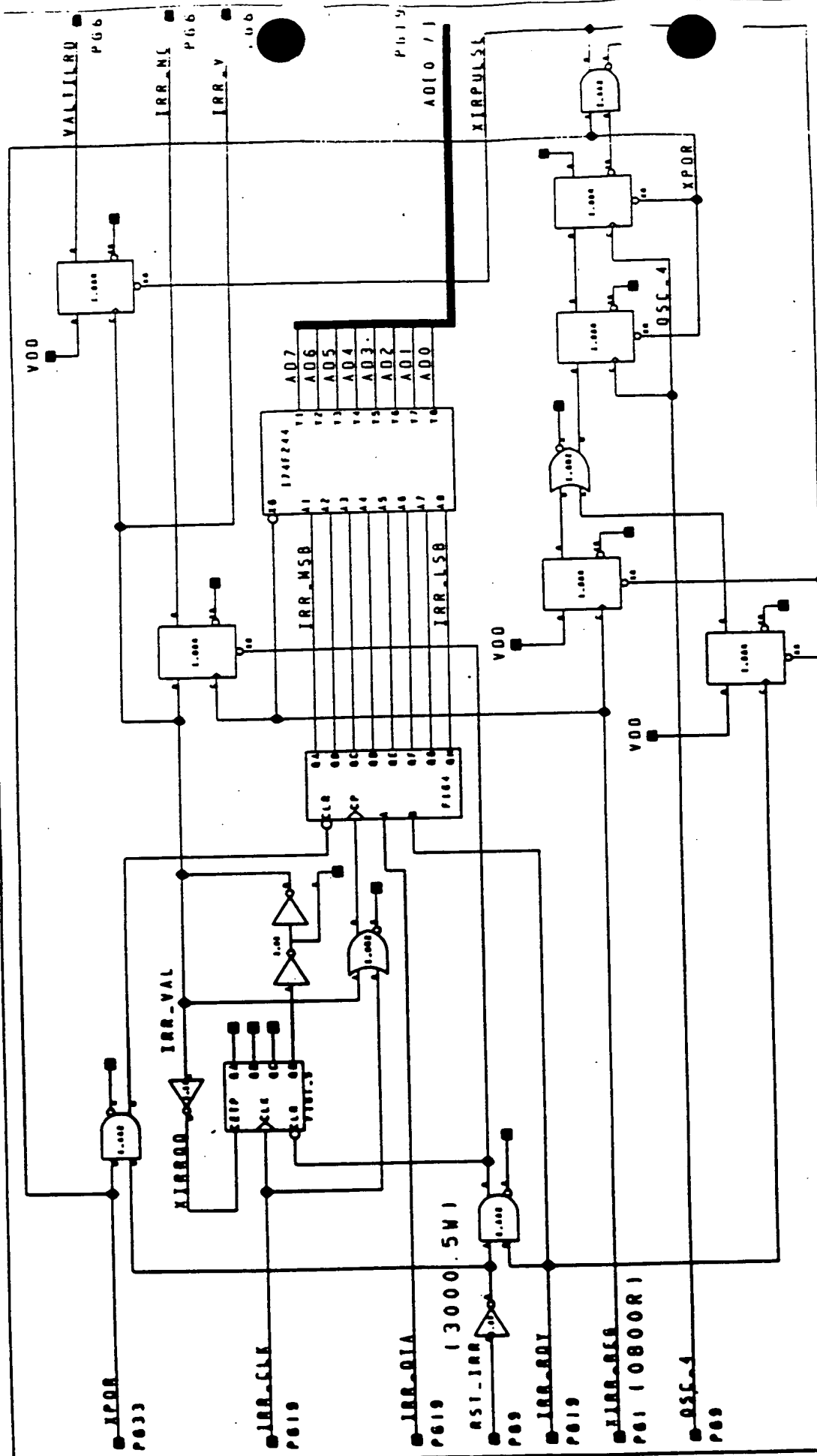
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COMMENTS IMP06110

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P/N 10-01-00003

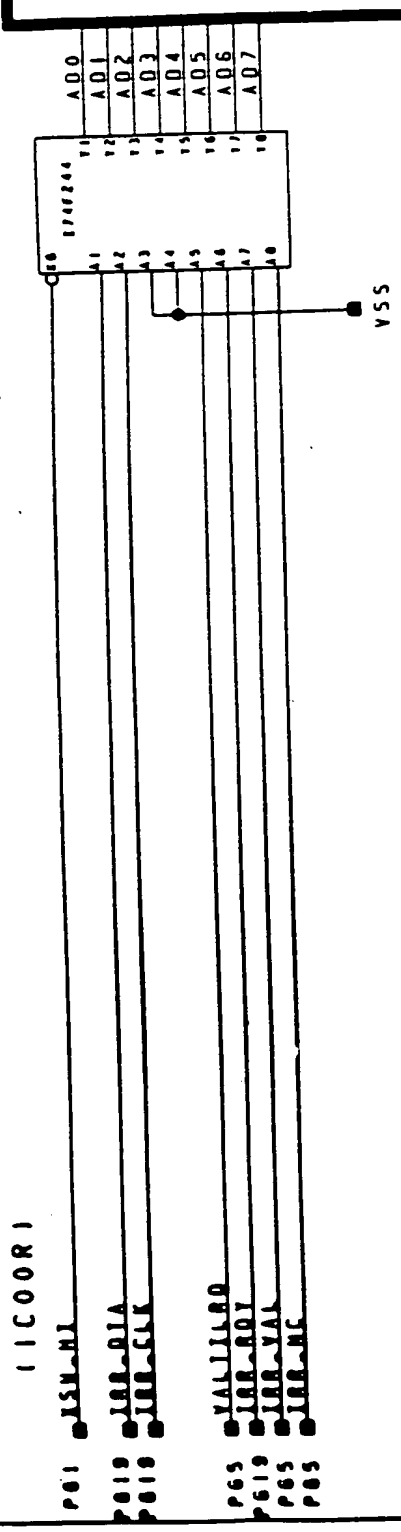


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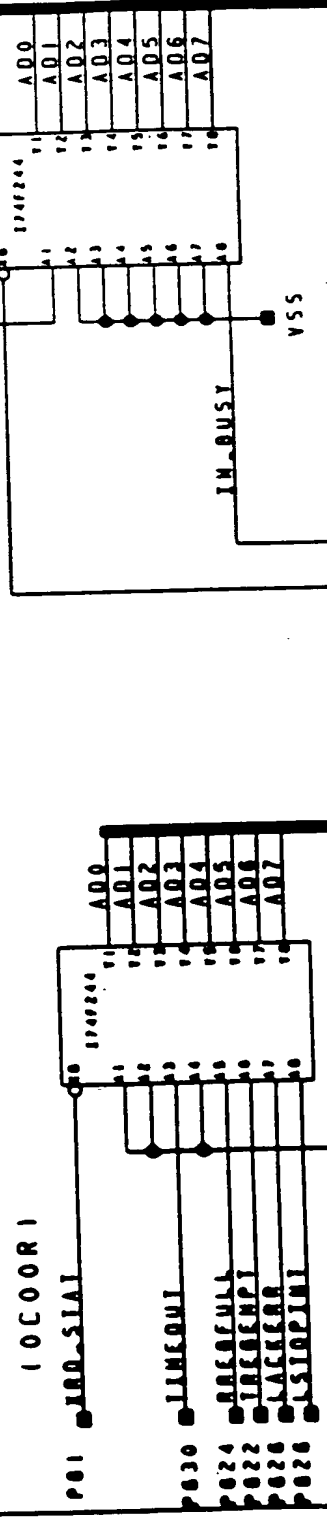
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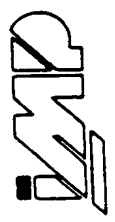
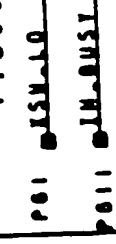
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VDD

ISB INTERRUPT STATUS REG



I. R. READ STATUS REG.



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PAGE: 6 OF 60

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REVISION: 1

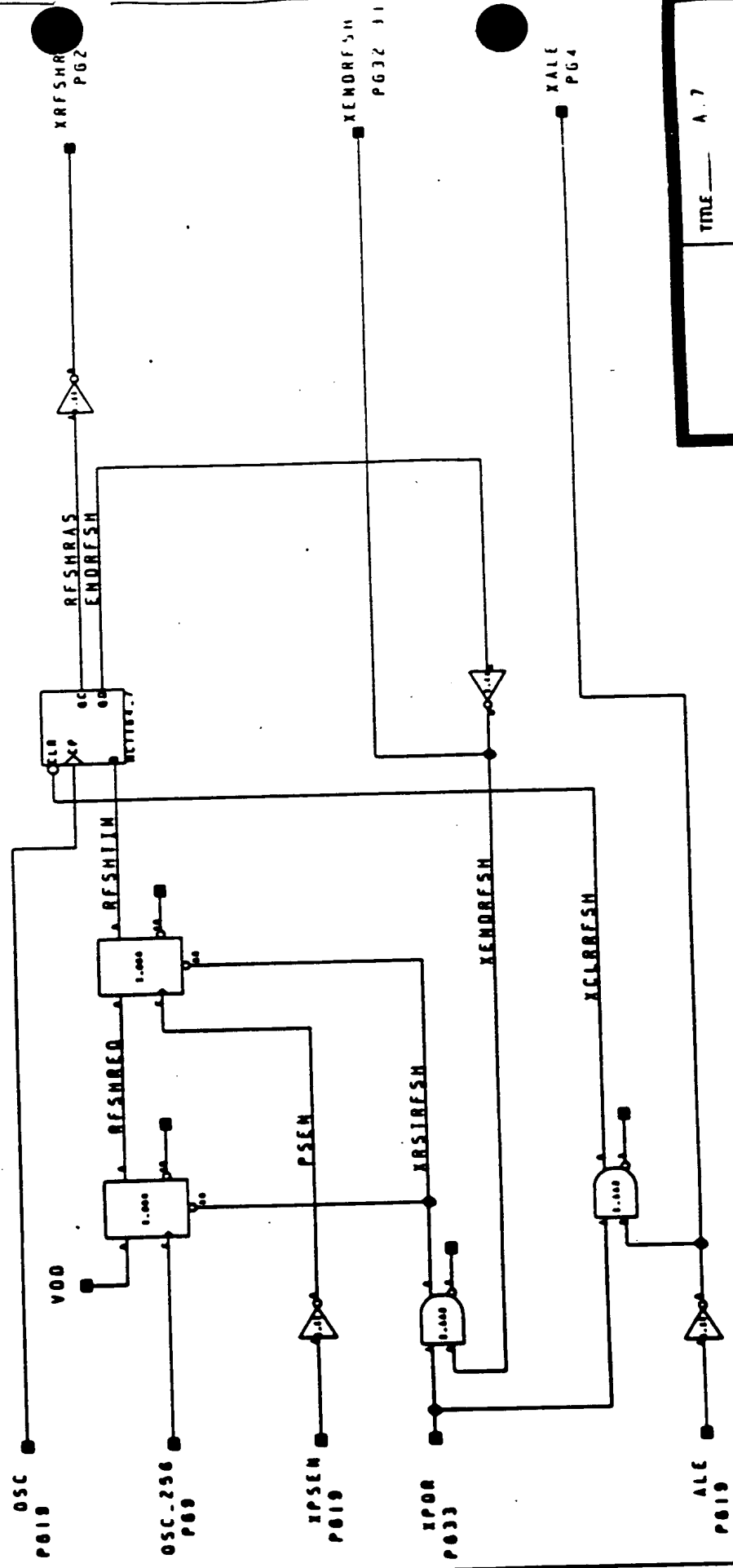
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SCHEMATIC DBE 1200

P/N 10-01-00003

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PSRAM REFRESH LOGIC



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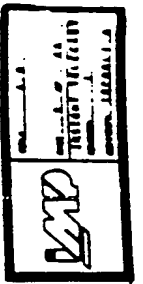
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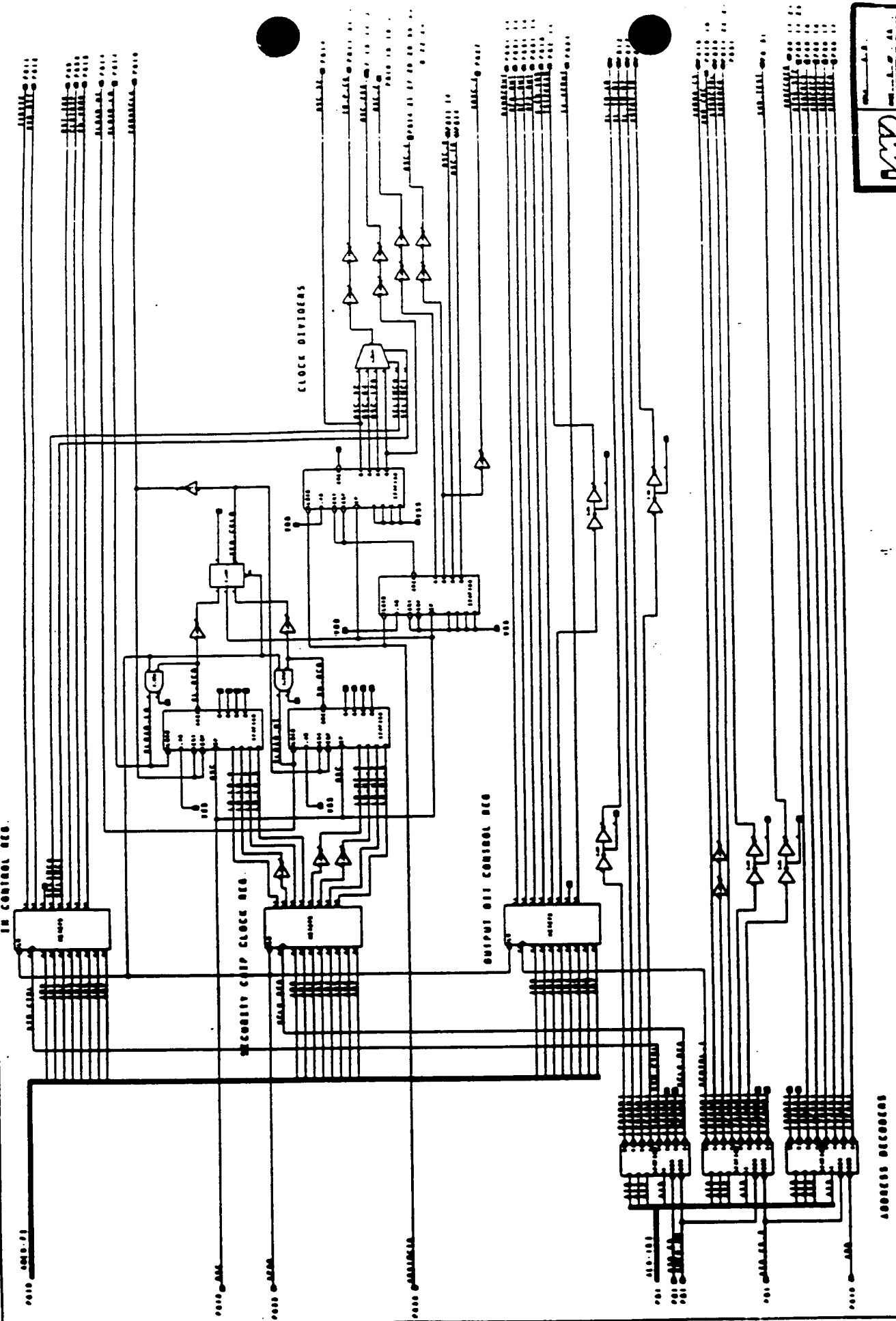
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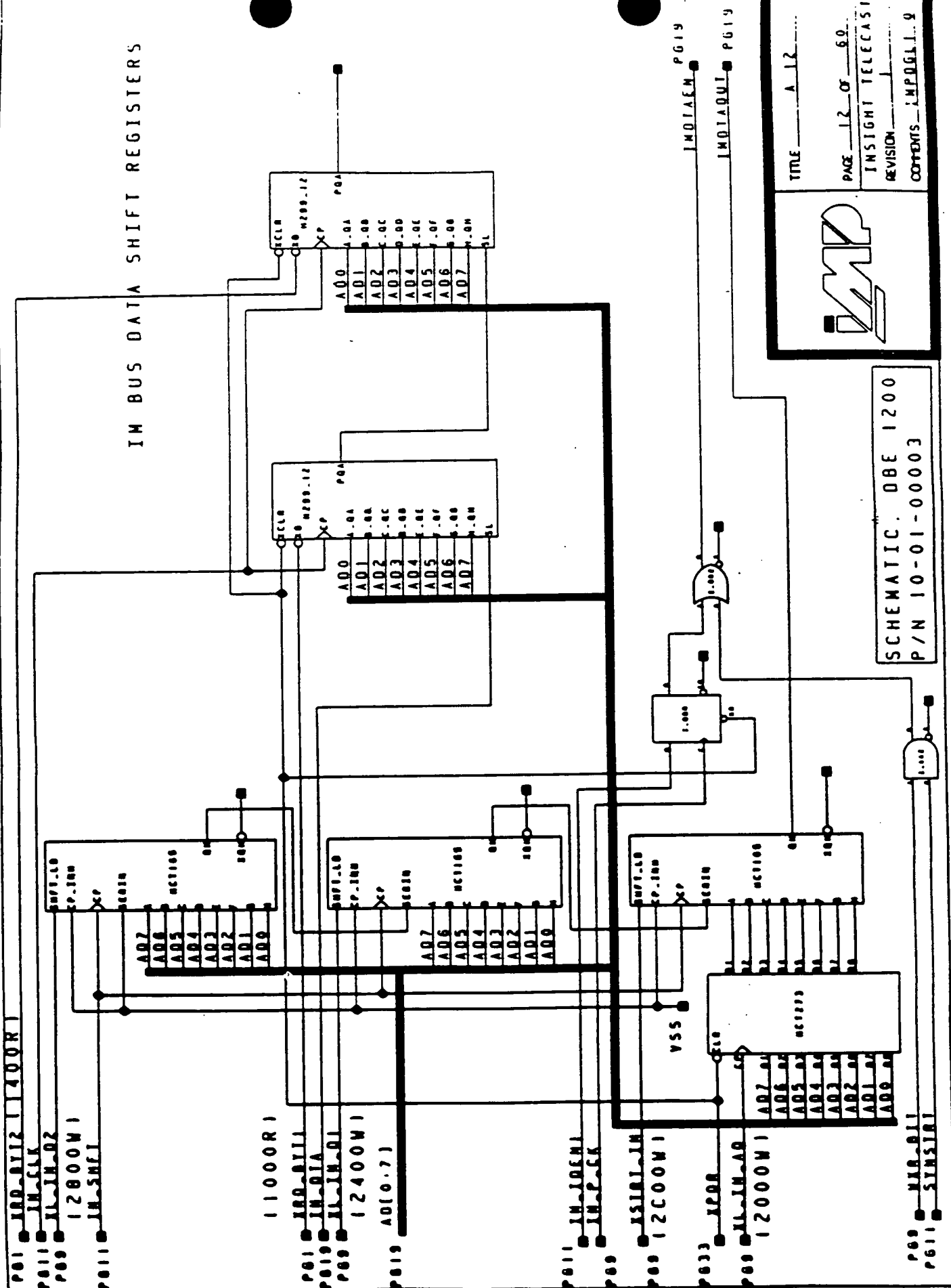
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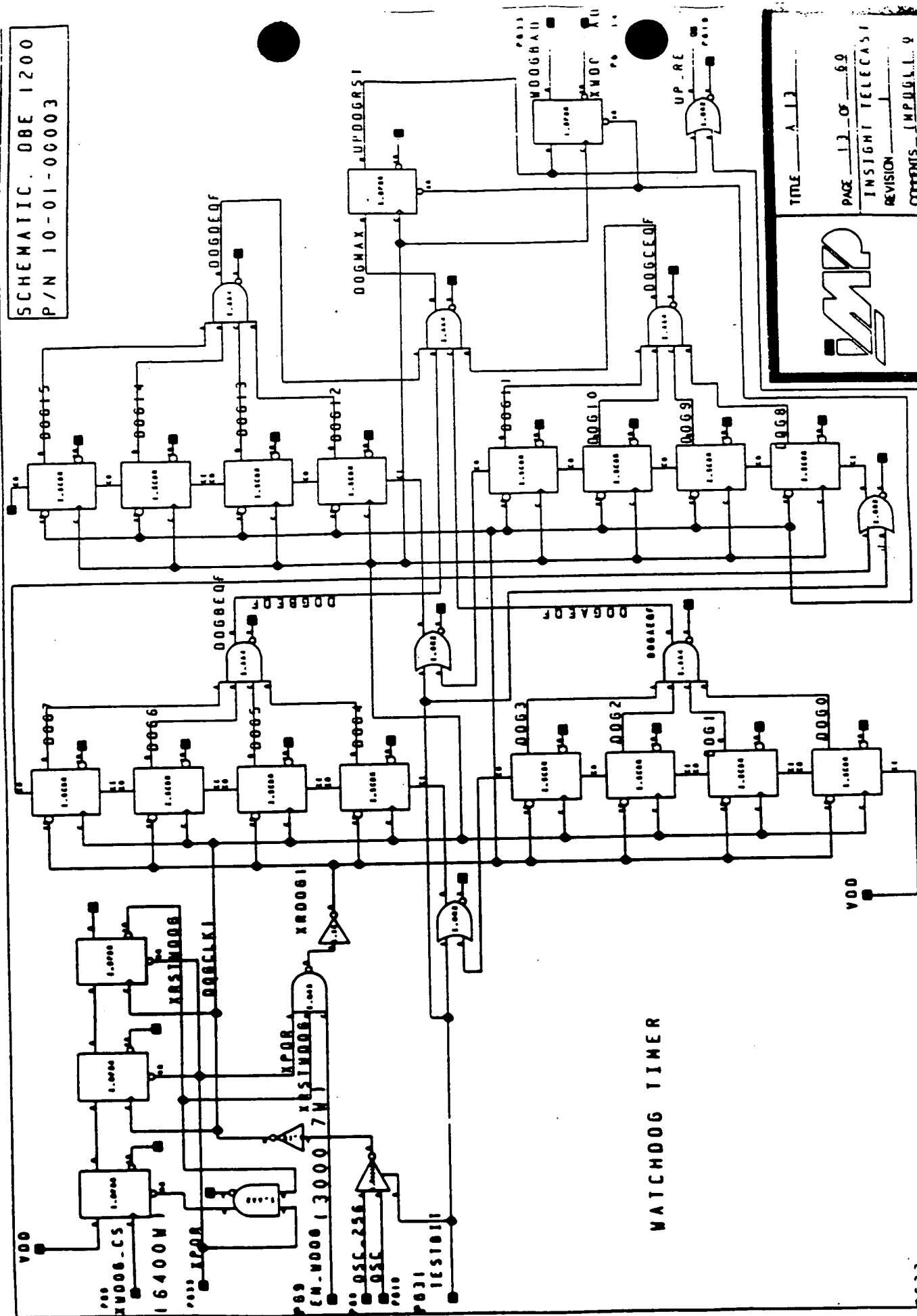


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WATCHDOG TIMER

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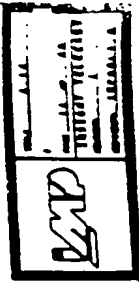
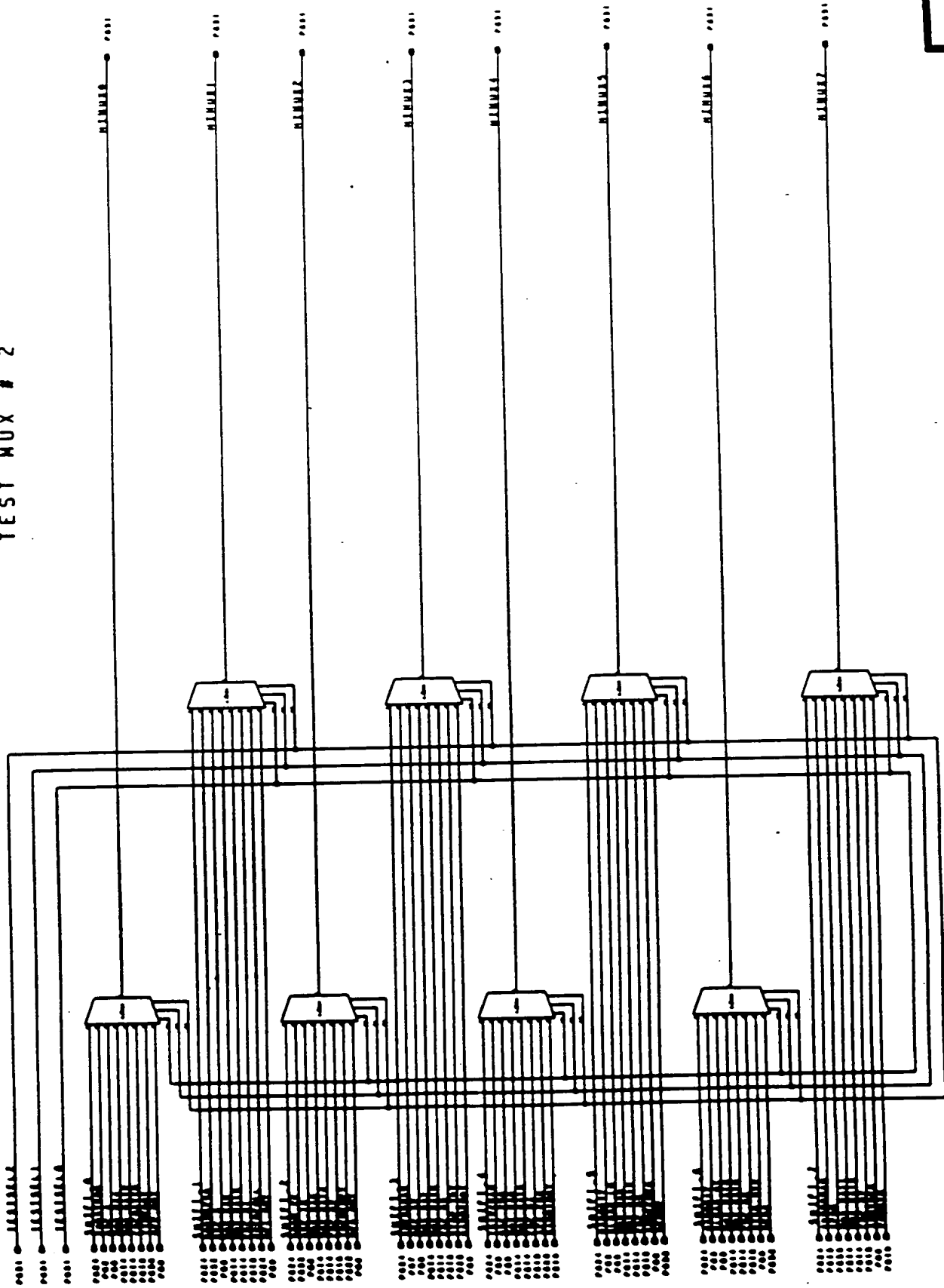
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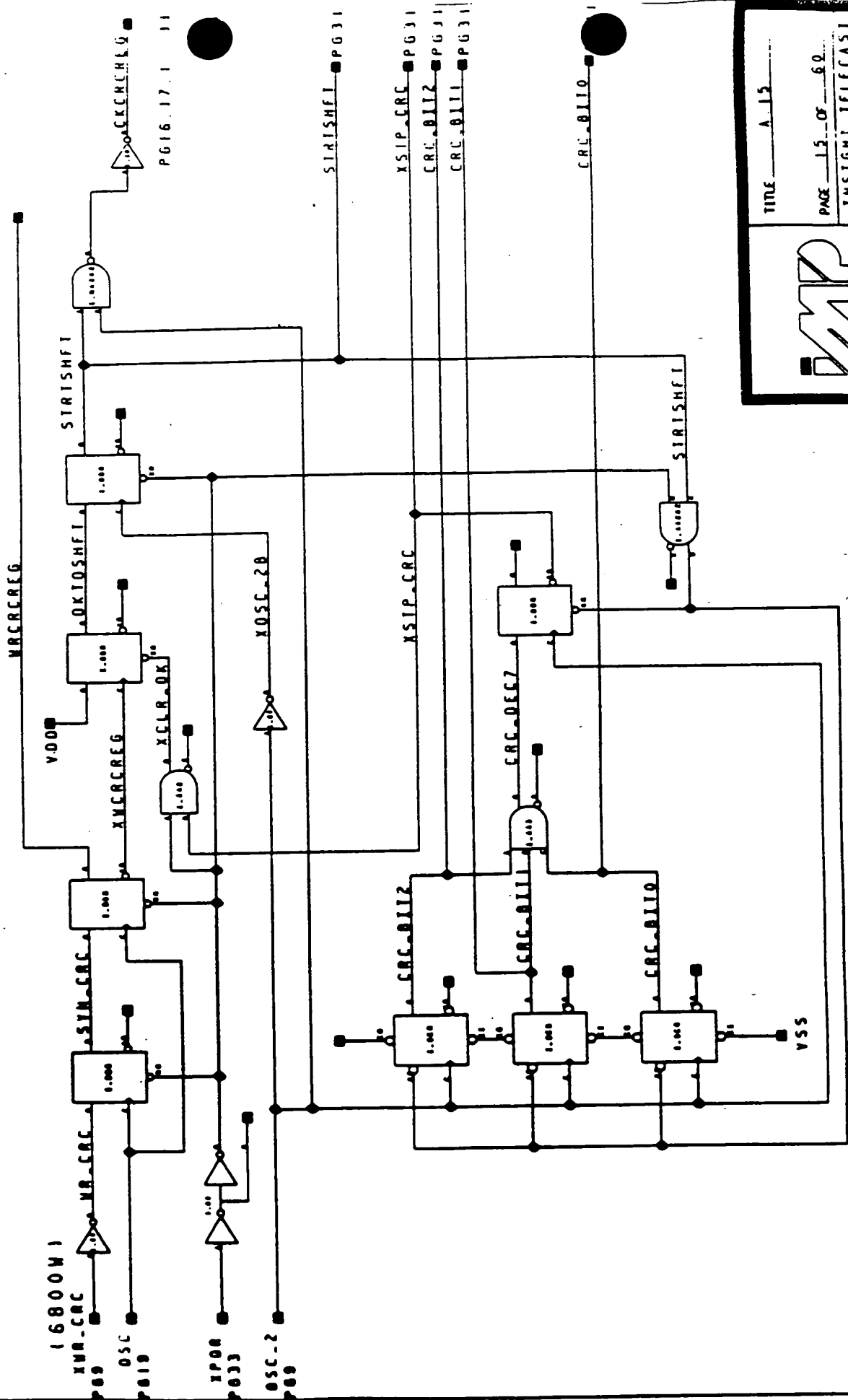
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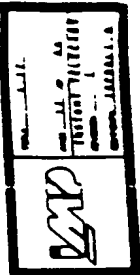


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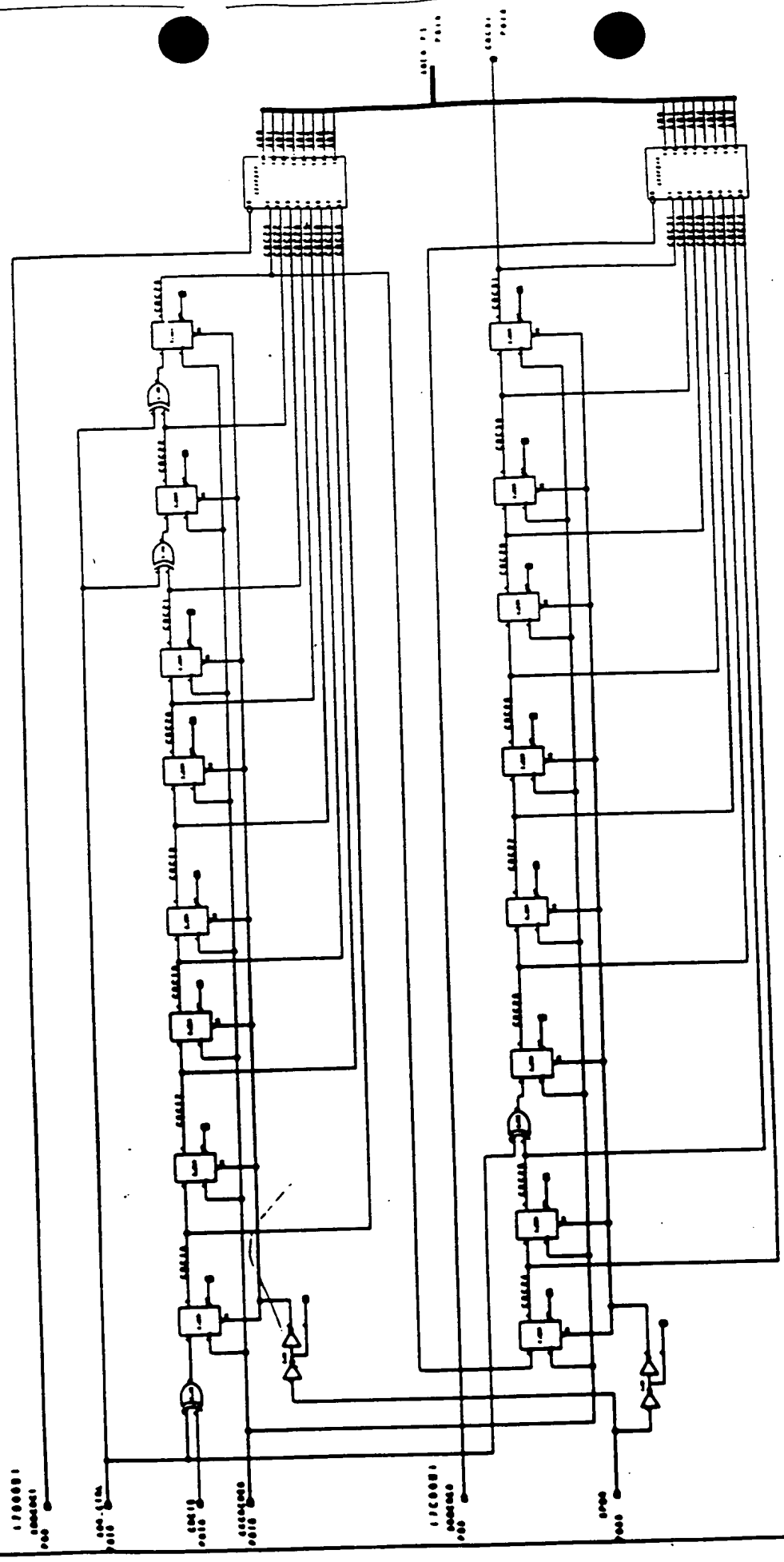
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PG33

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PG15

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AD7

CRC-07

CRC-07 PG14

AD6

CRC-06

CRC-06 PG14

AD5

CRC-05

CRC-05 PG14

AD4

CRC-04

CRC-04 PG14

AD3

CRC-03

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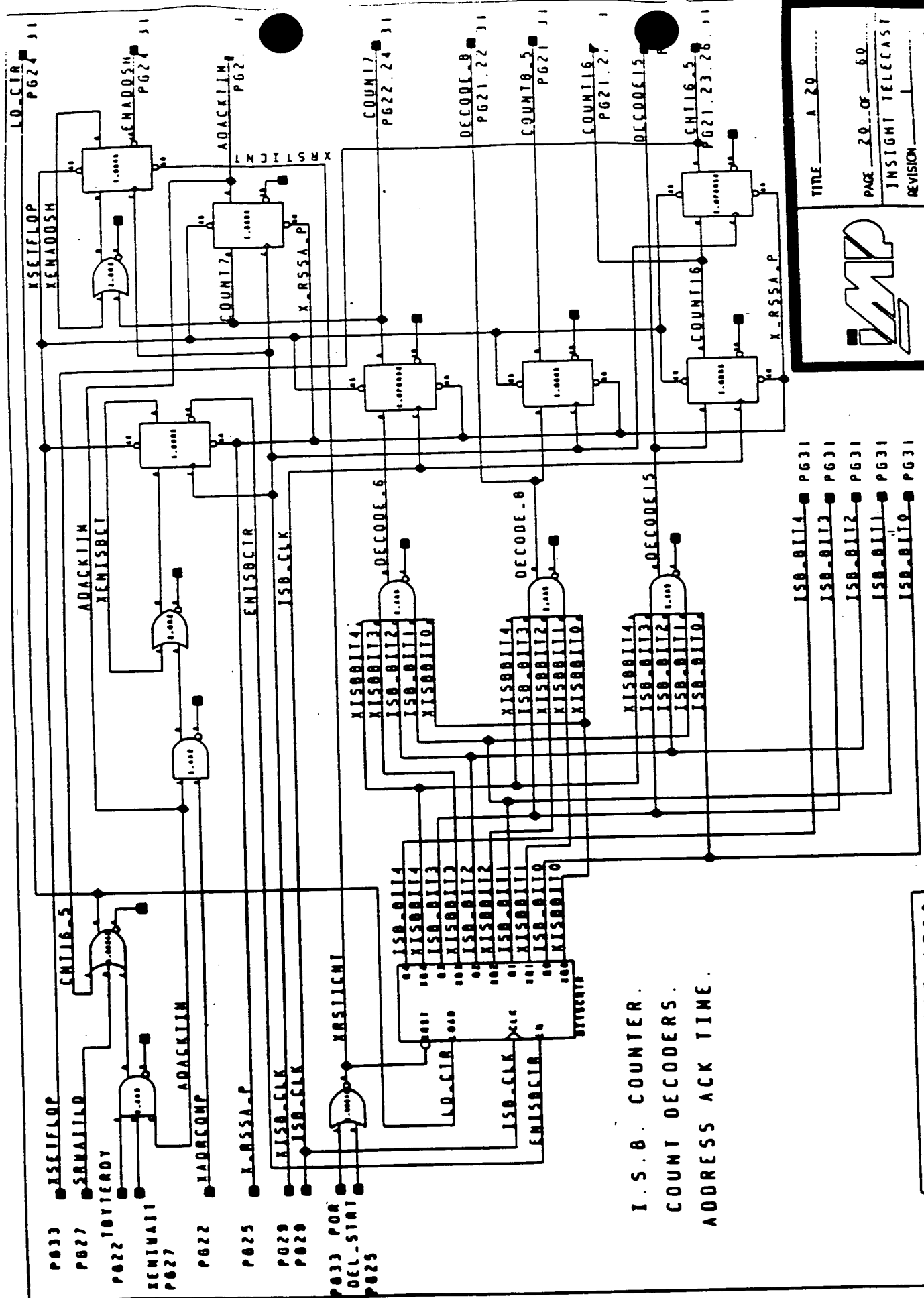
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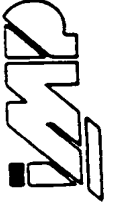
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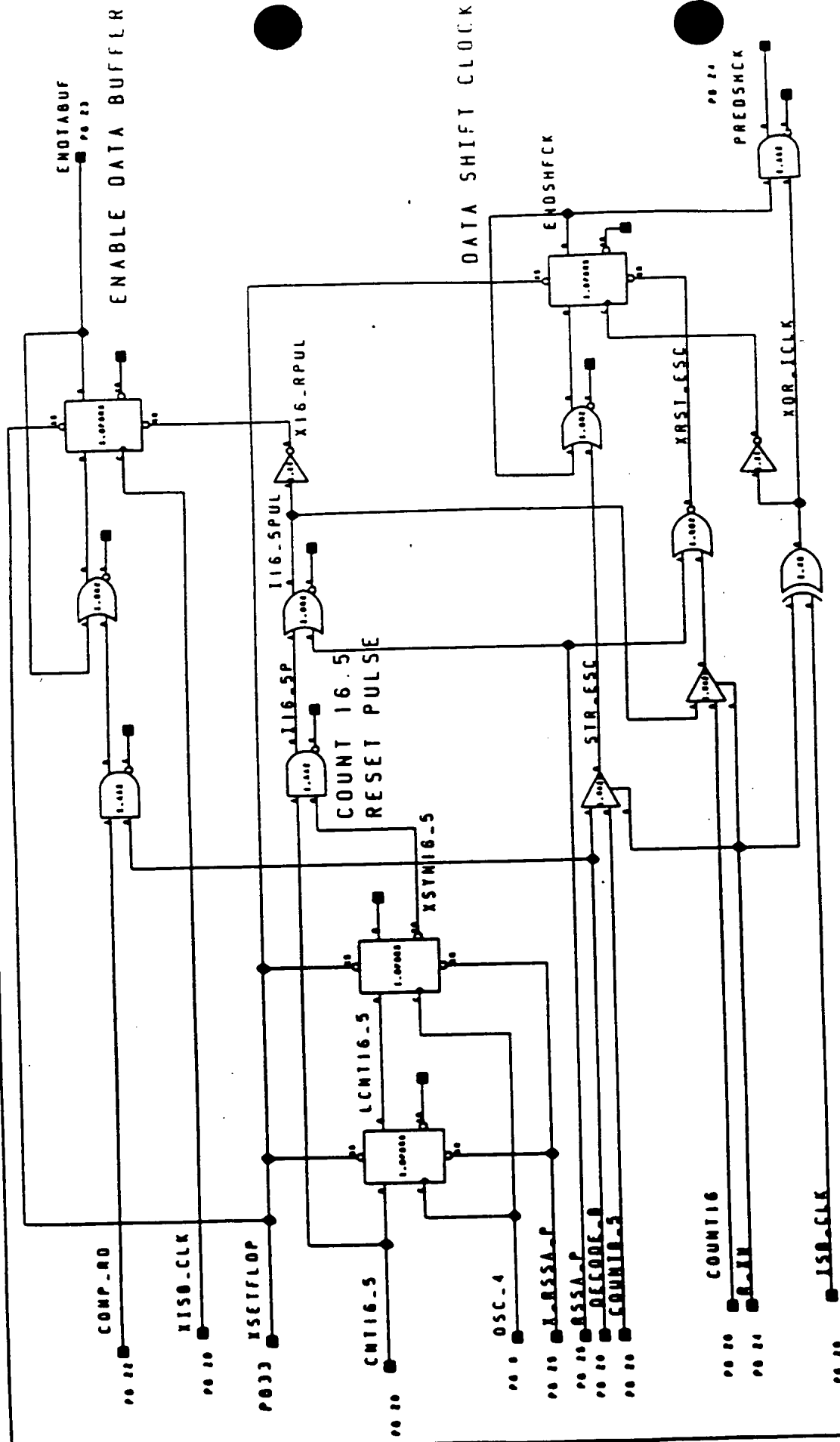
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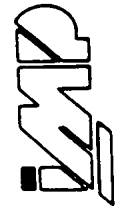
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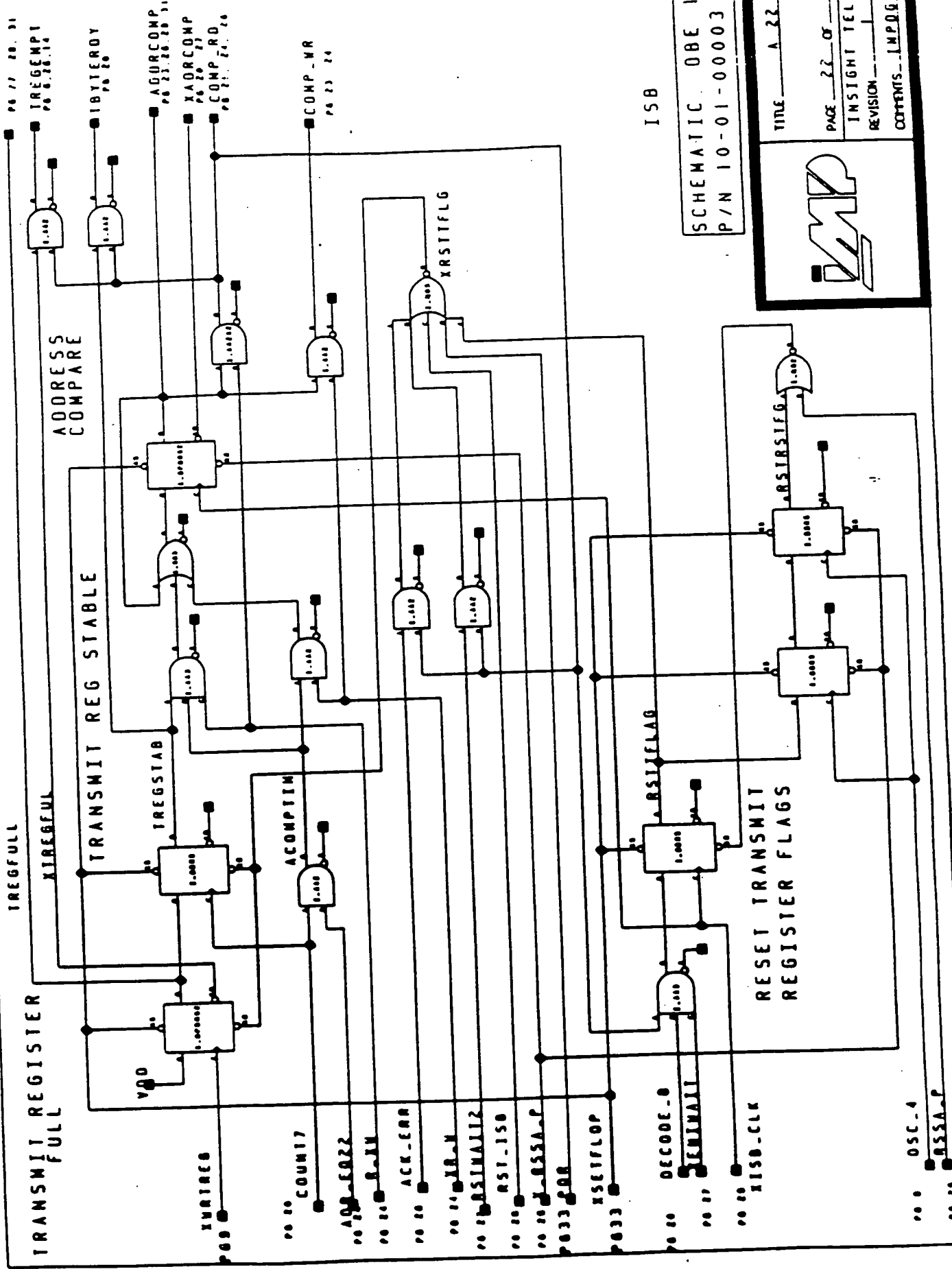


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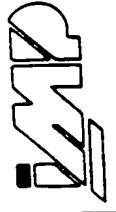
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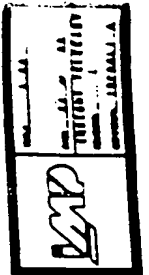


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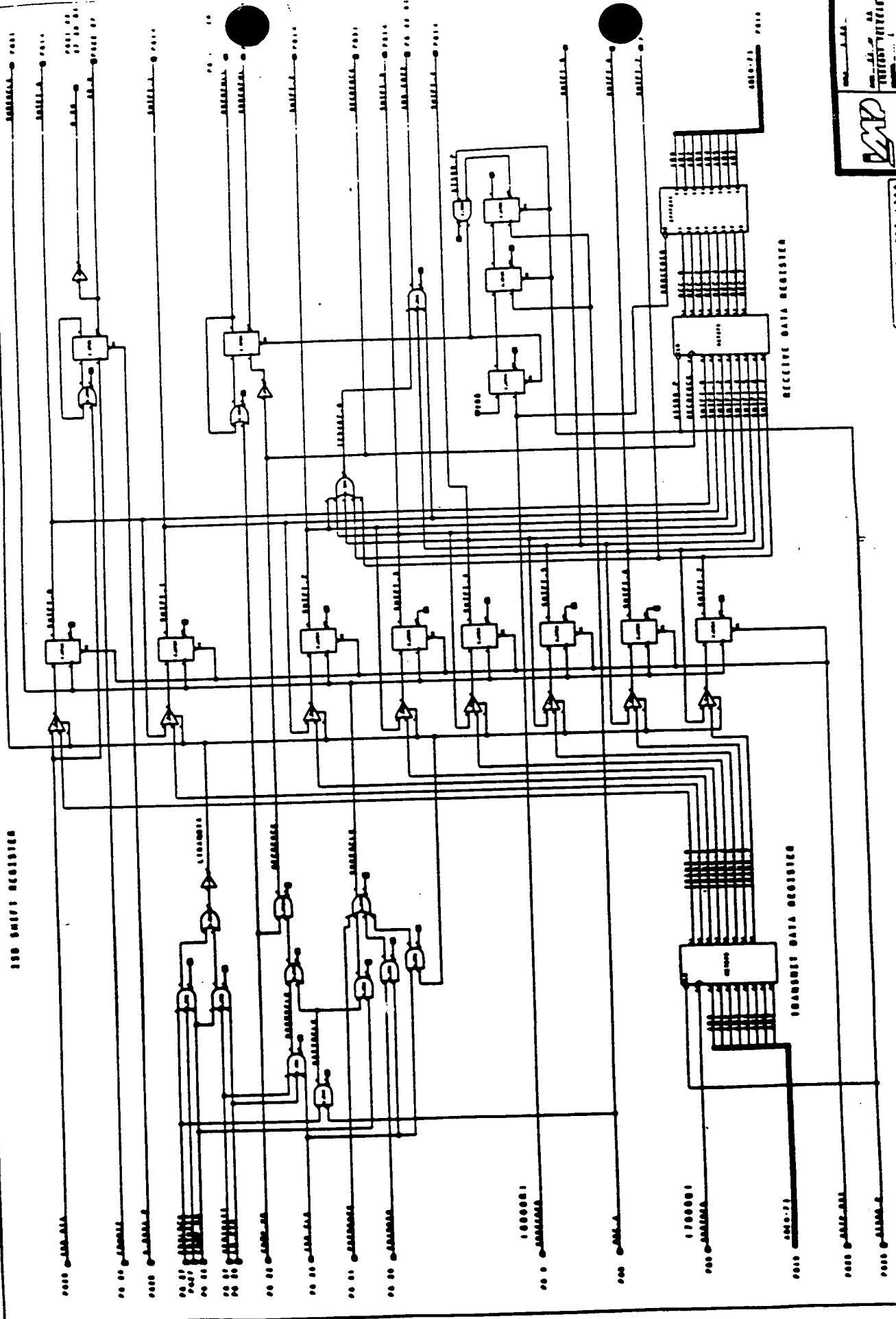
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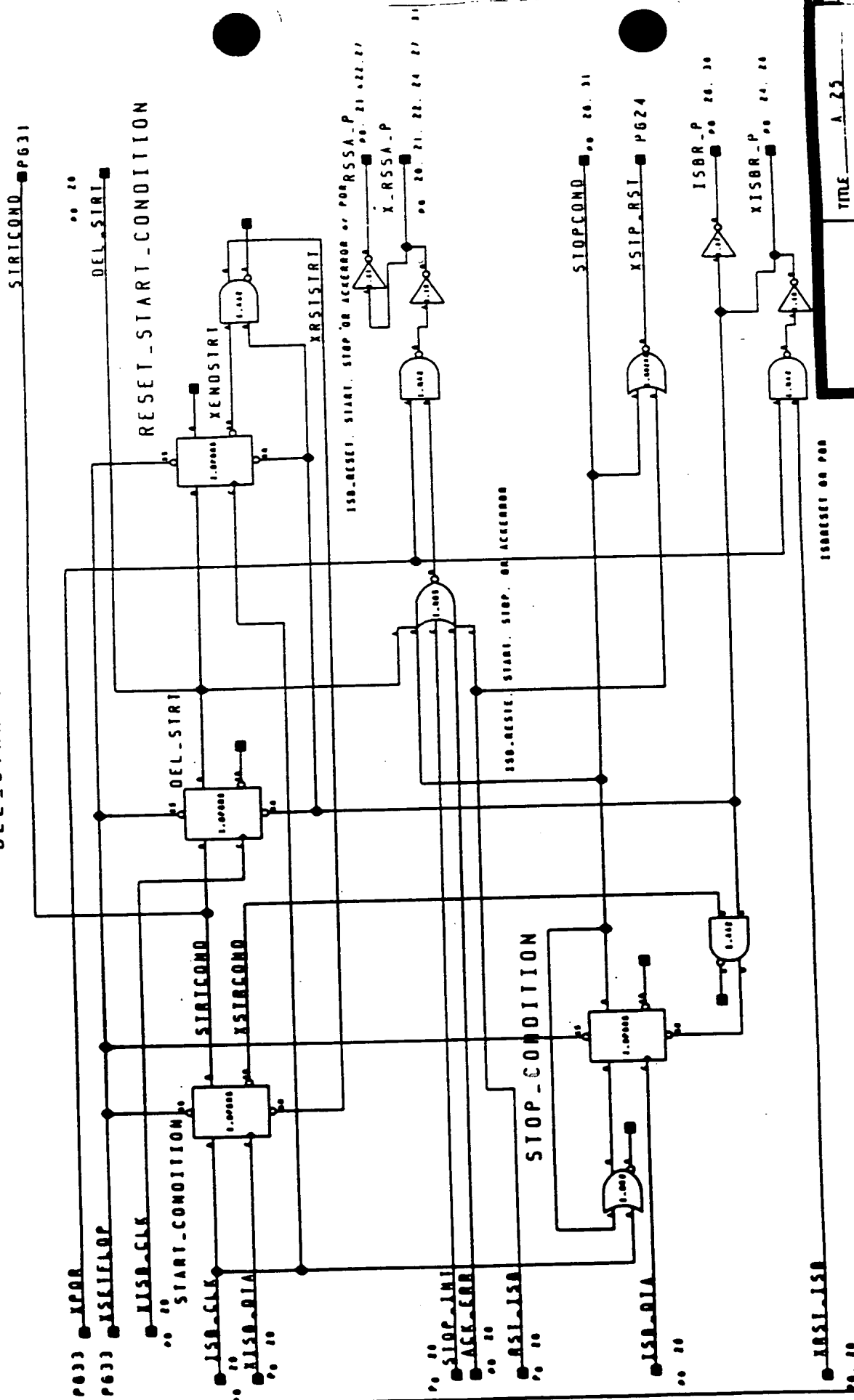
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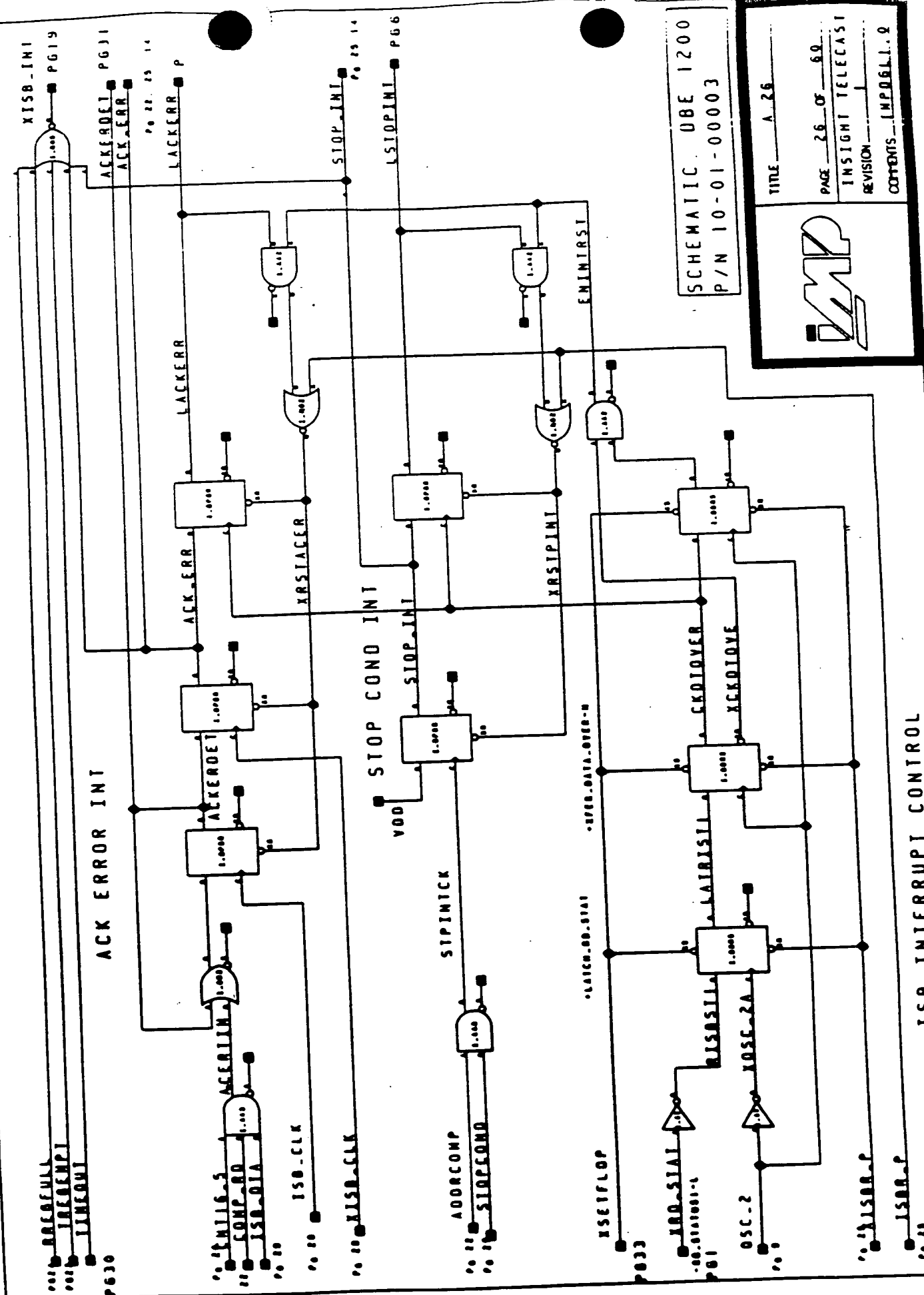


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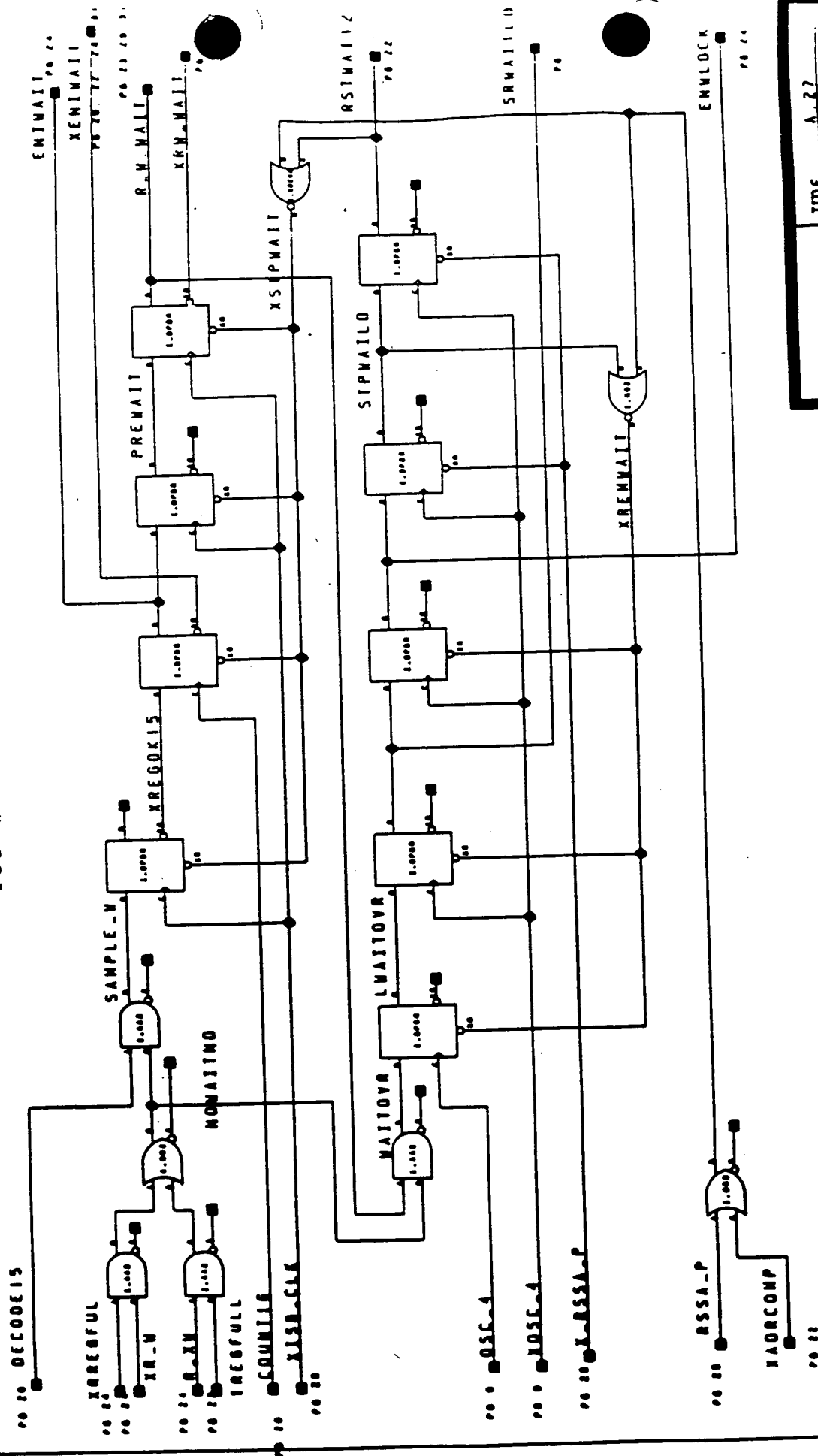


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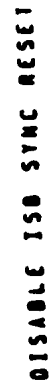
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RESET SYNCHRONIZER



STANDARD



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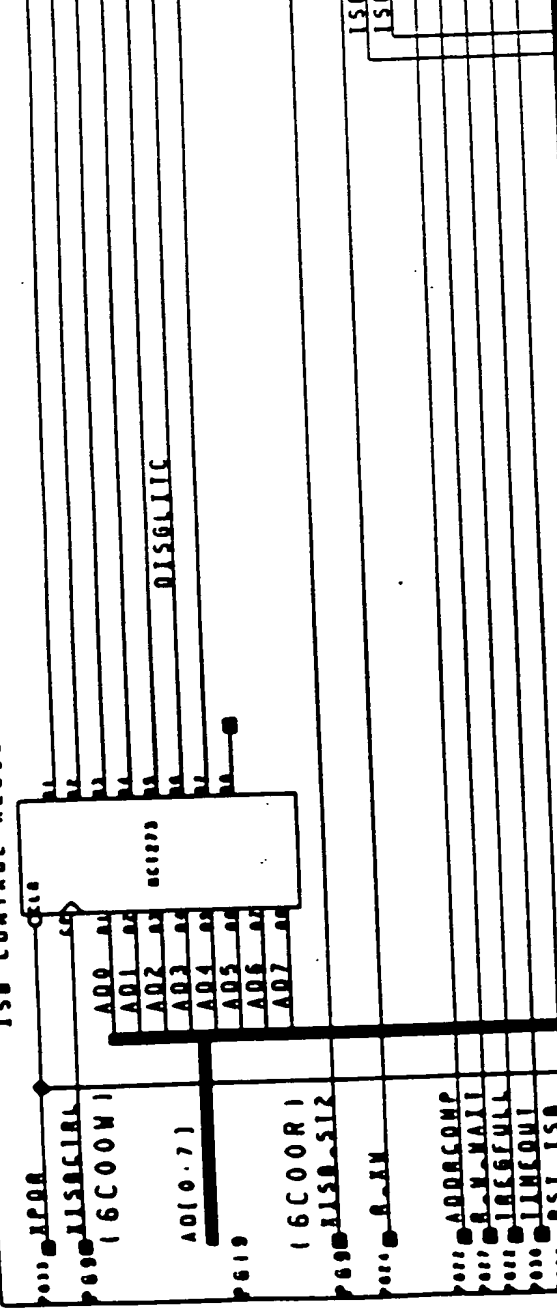
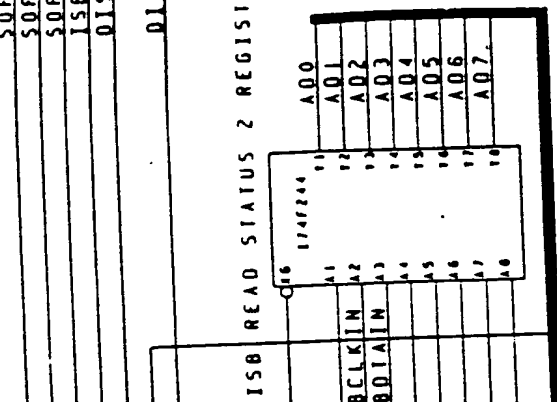
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 SOFT_CLK PG21
 SOFT_DIA PG21
 ISB5011 PG23
 DISISBRS PG28
 DIS_IQV1 PG30



ISB_CLK PG19, 20, 21
 24, 25, 26, 28
 XISB_CLK PG19, 20, 21
 22, 25, 26, 27

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ISB CLOCK DEGLITCHERS

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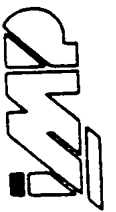


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ISB TIMEOUT

TIMEOUT



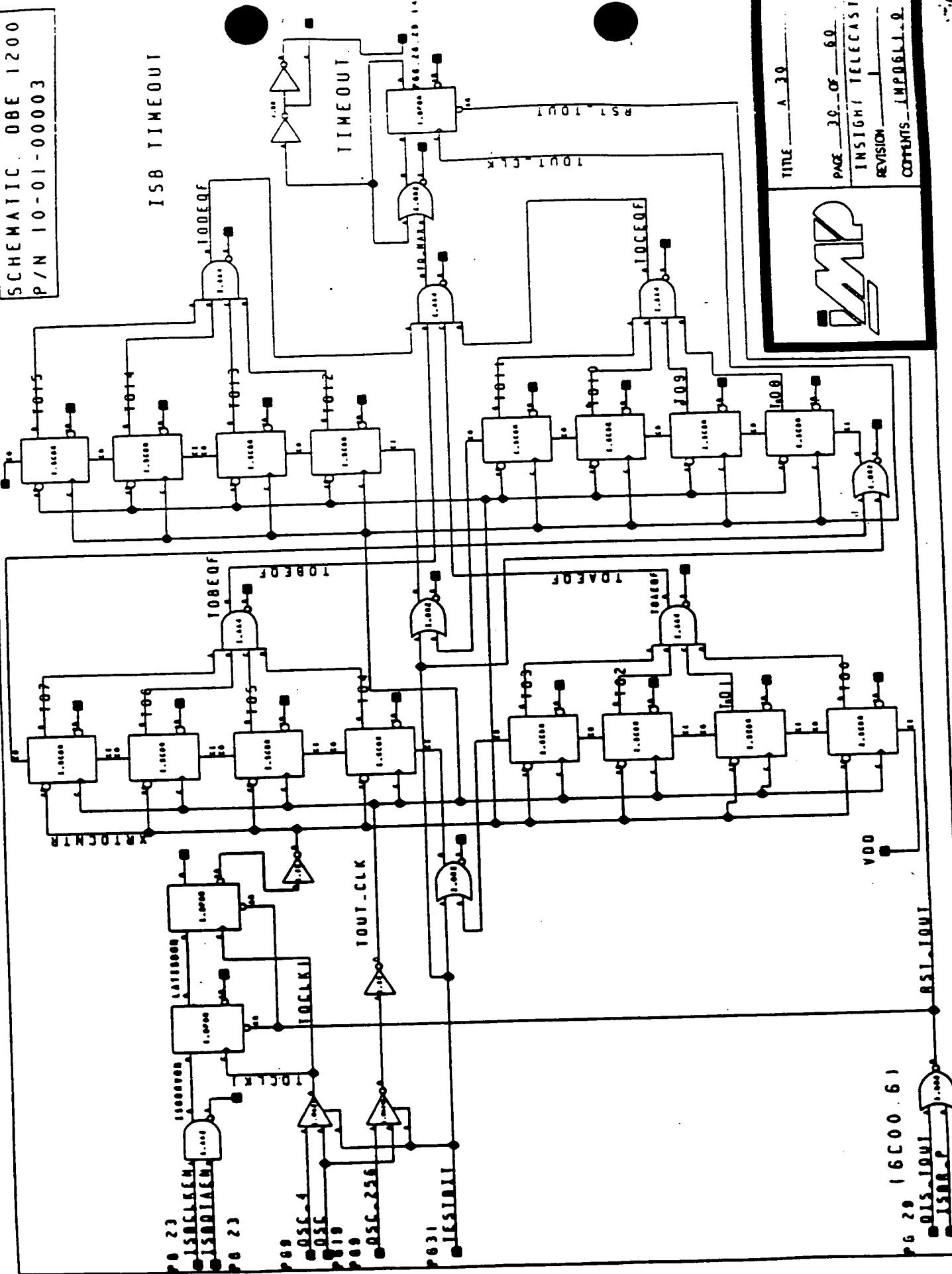
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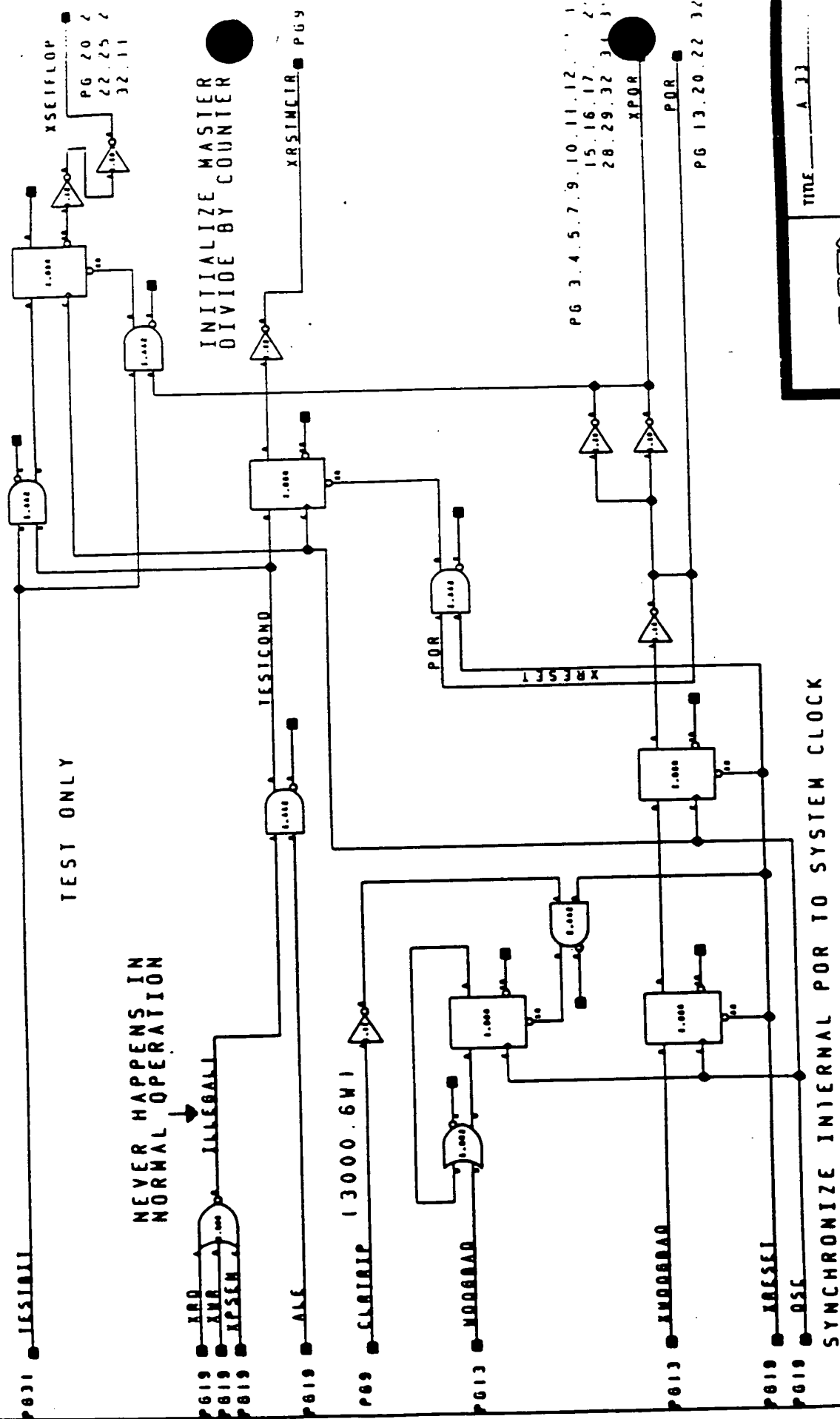


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P/N 10-01-00003

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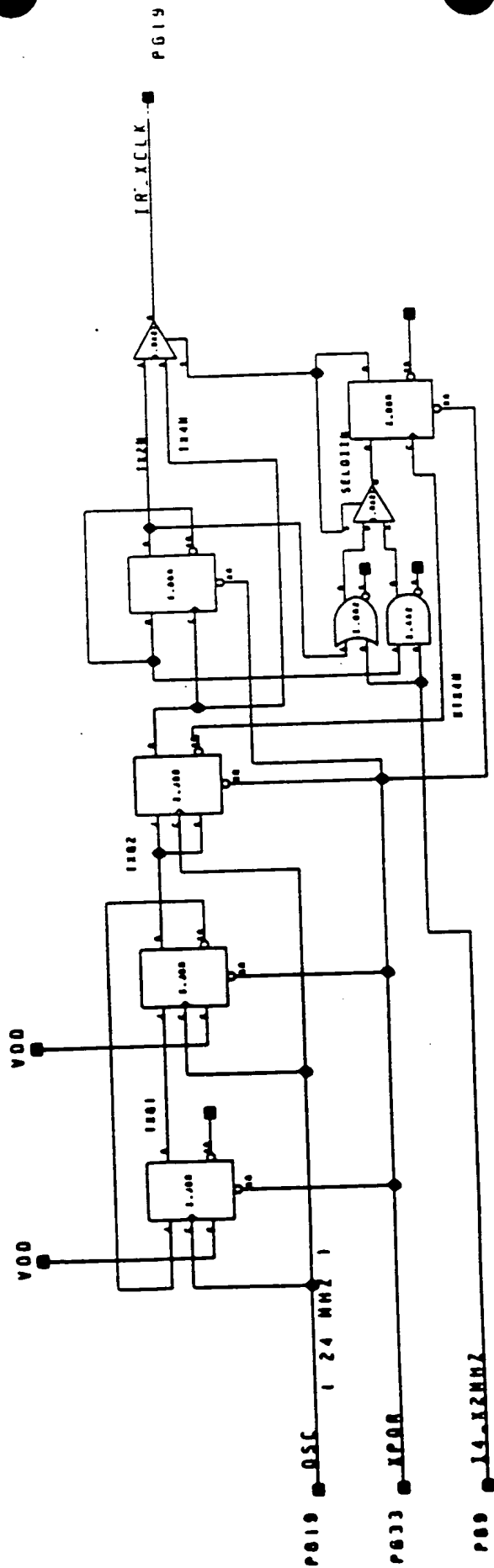
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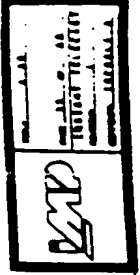
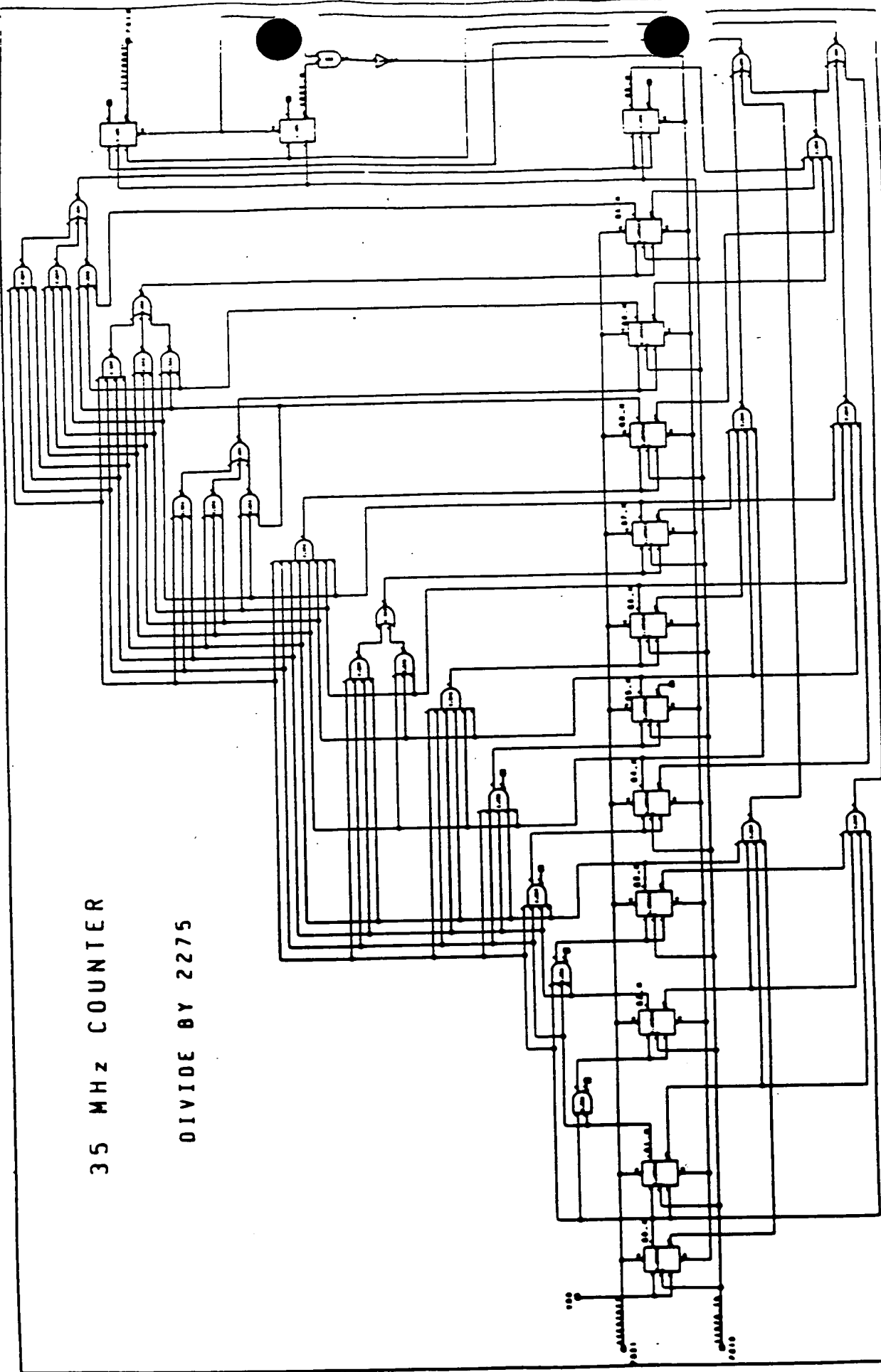


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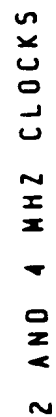
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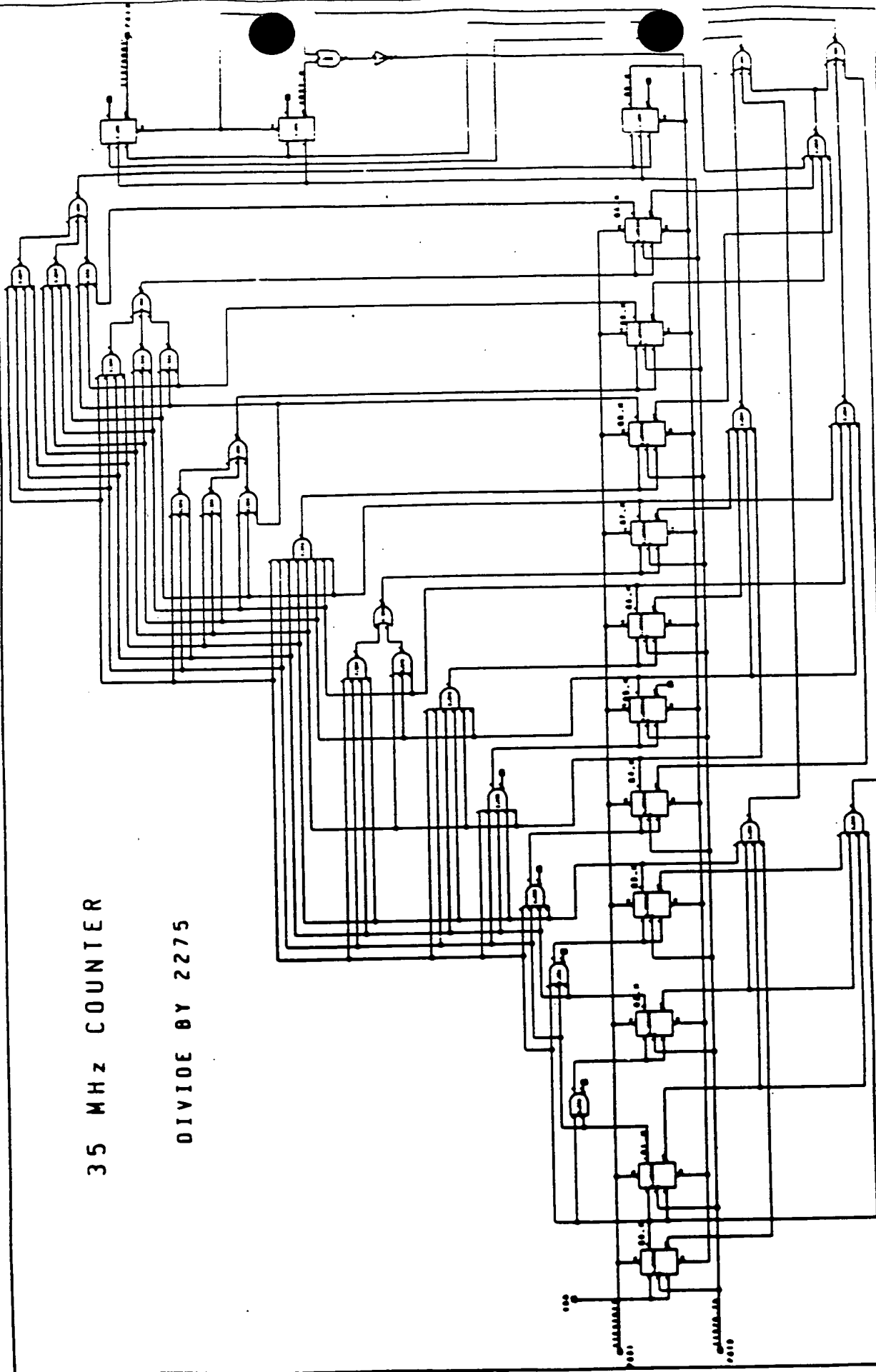


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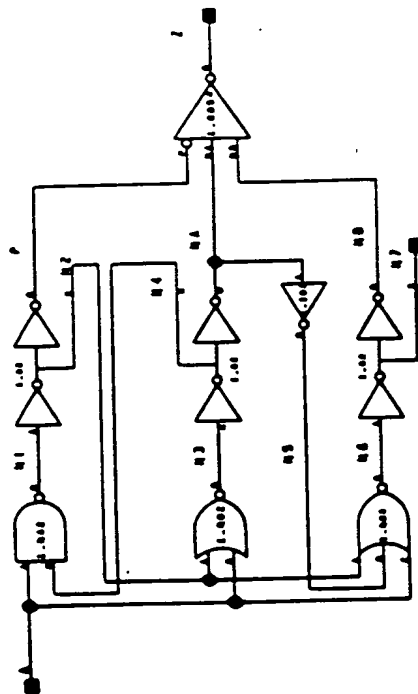
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 5. Checked
 6. Approved
 7. Part No.
 8. Rev.
 9. Qty.
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 13. Remarks



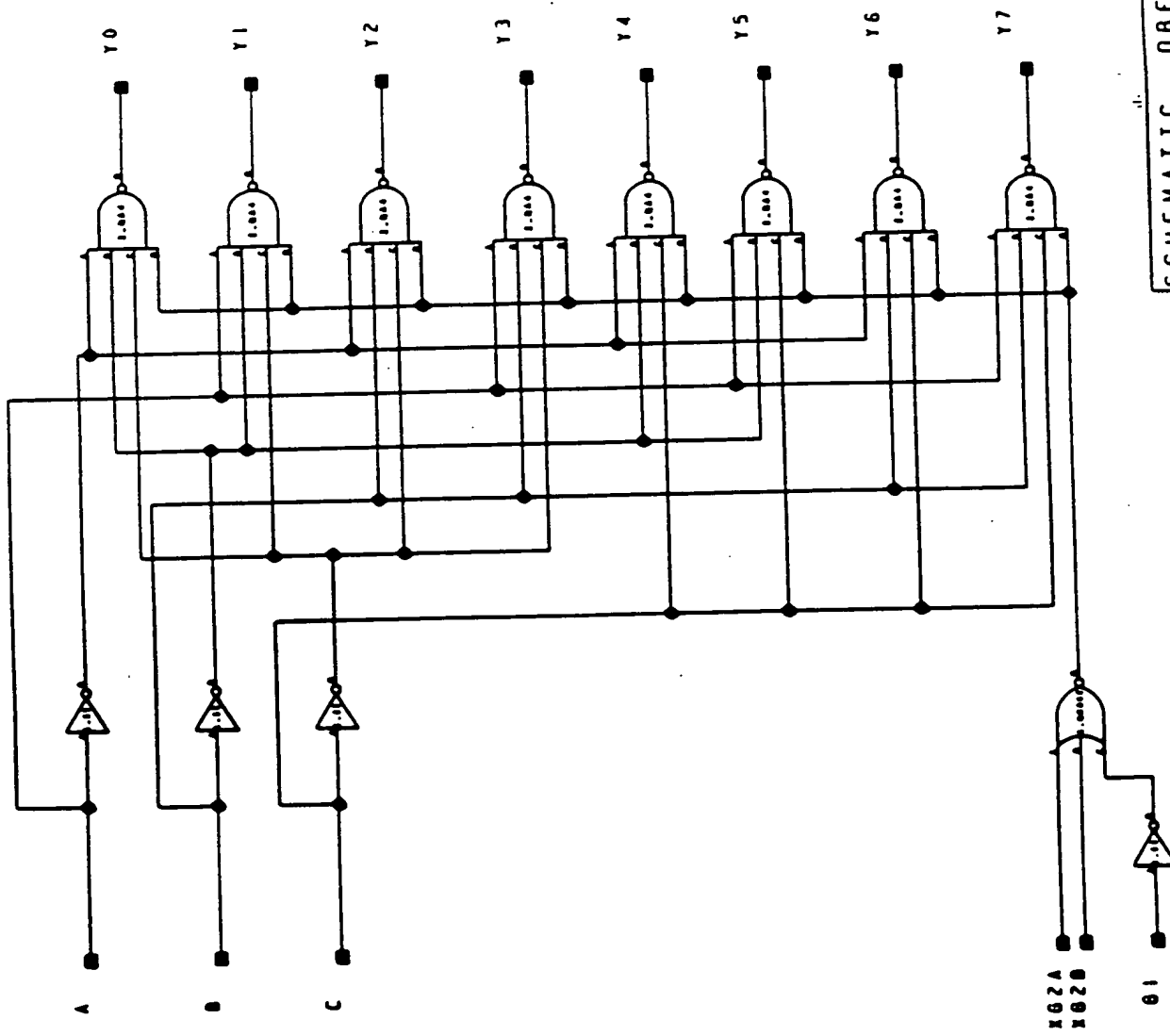
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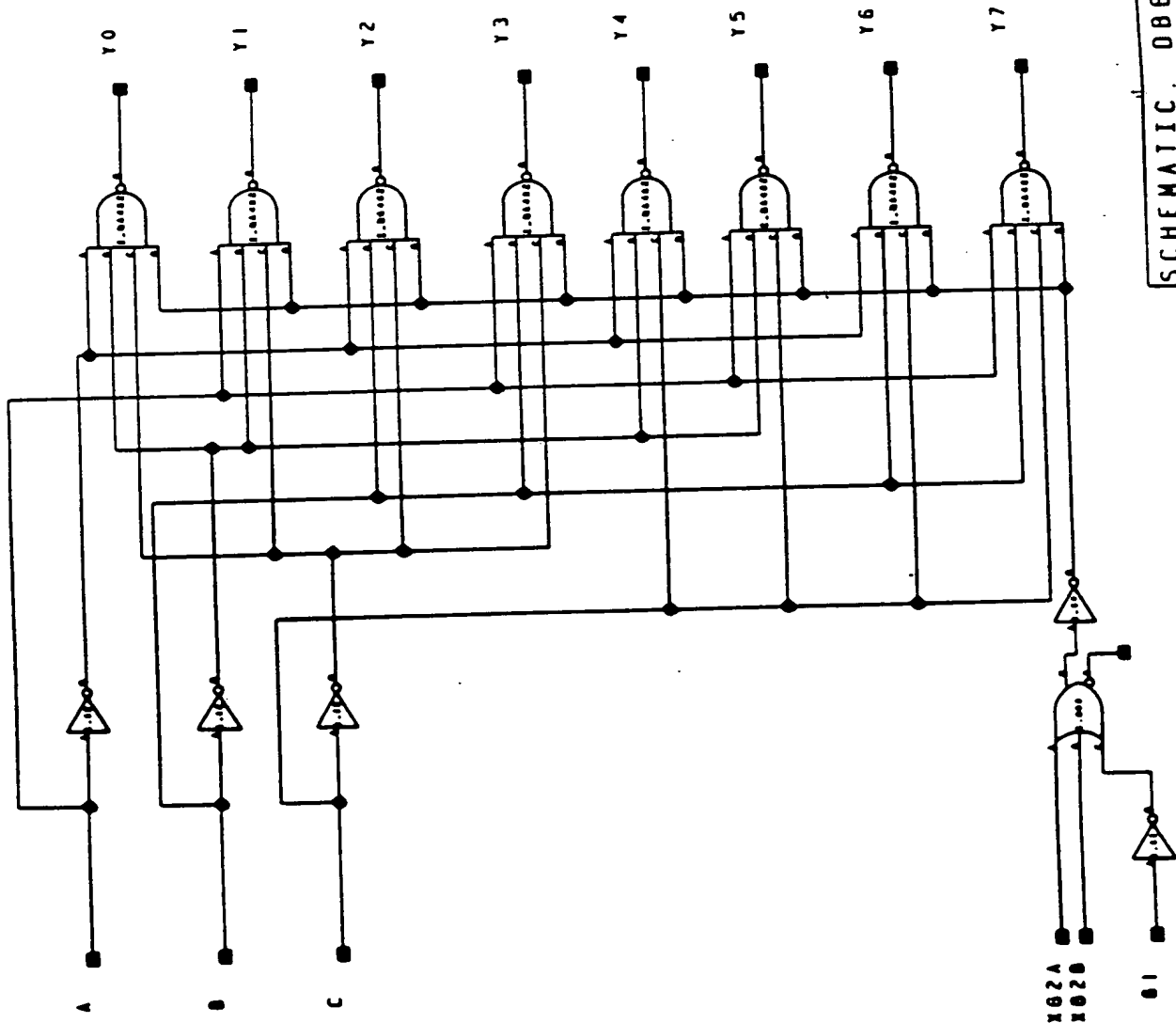
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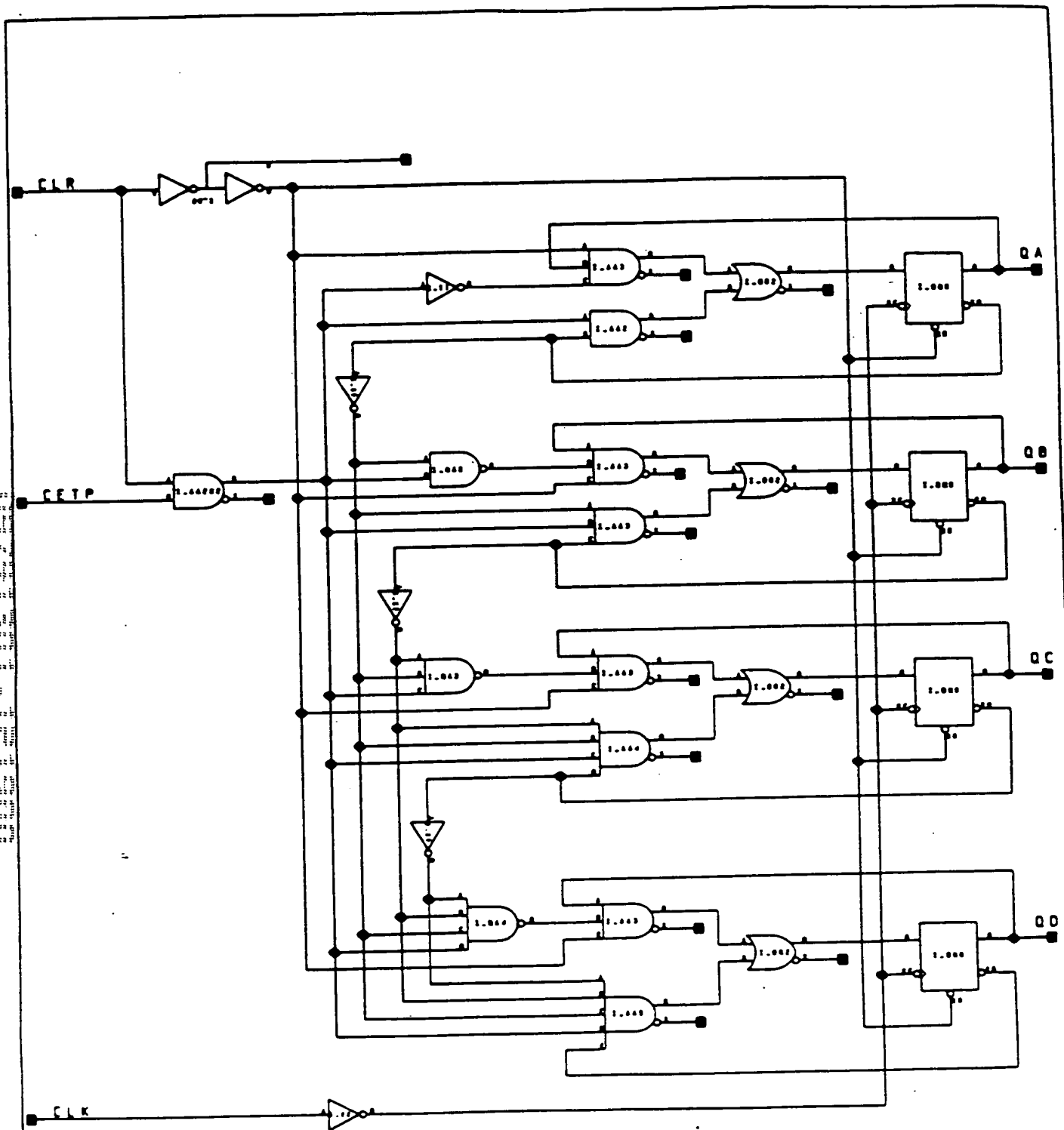
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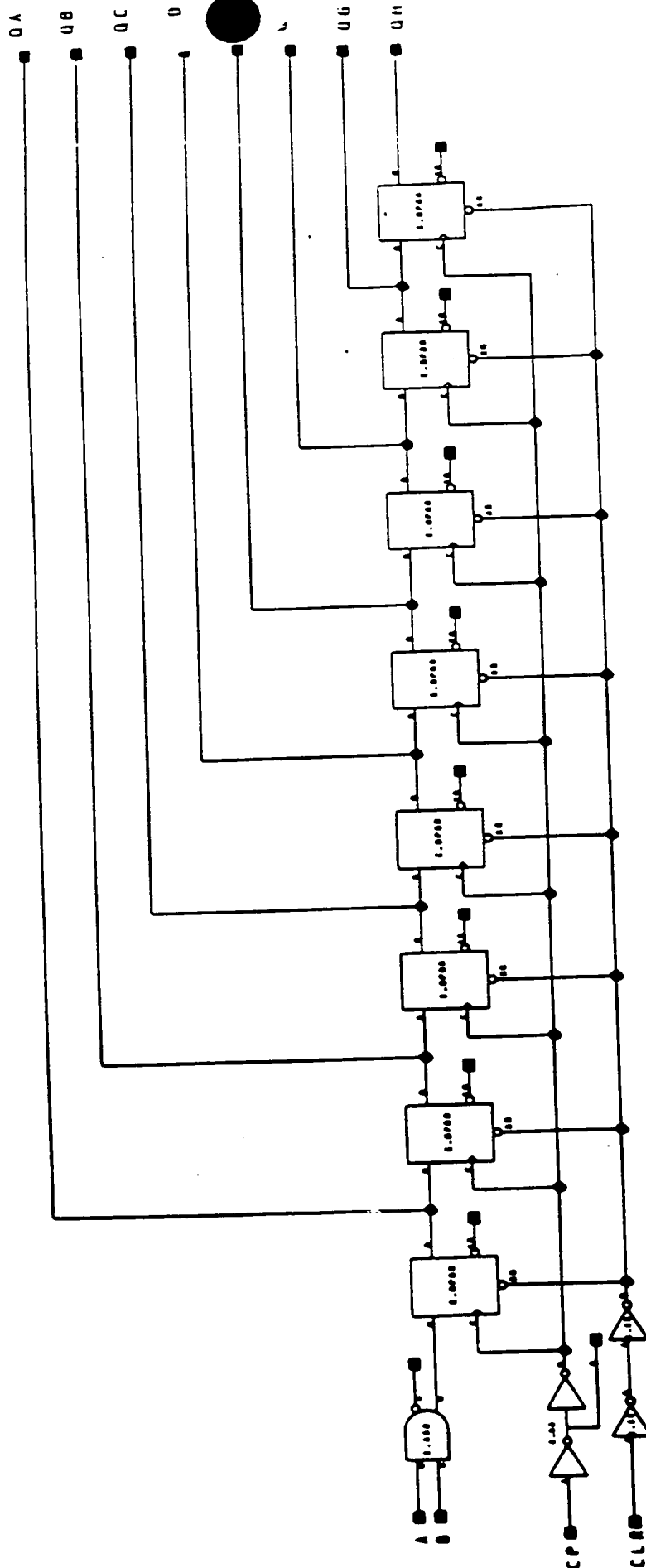
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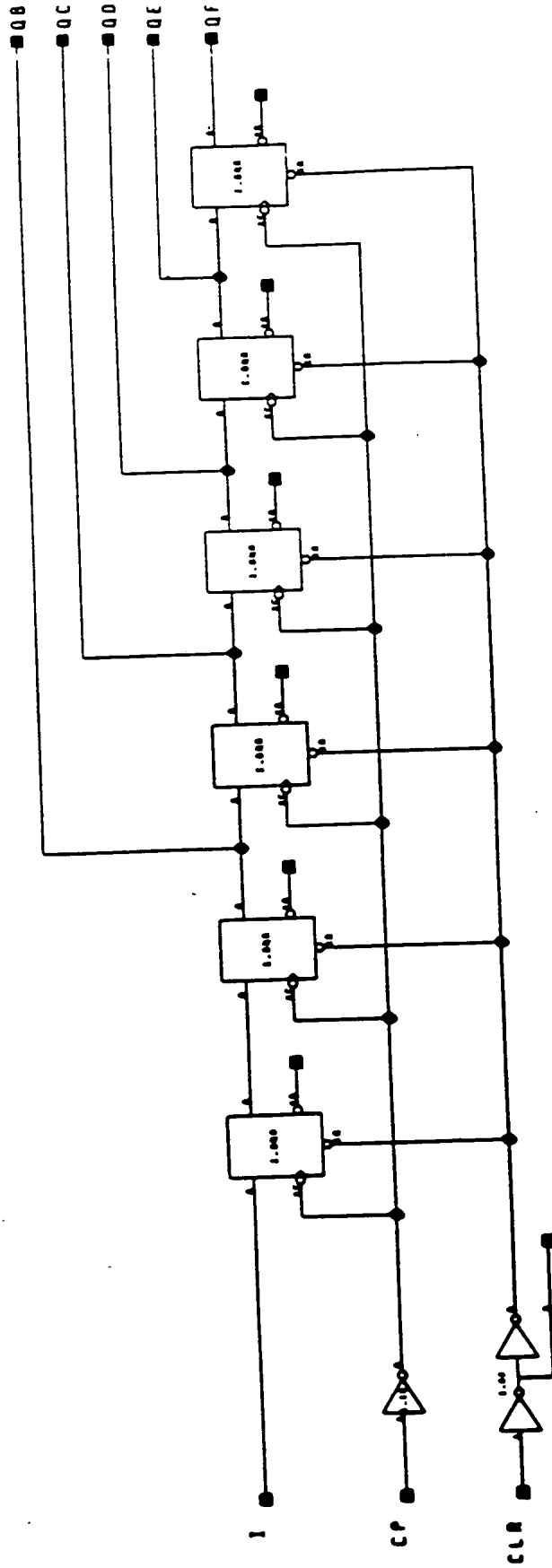
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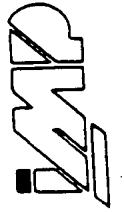
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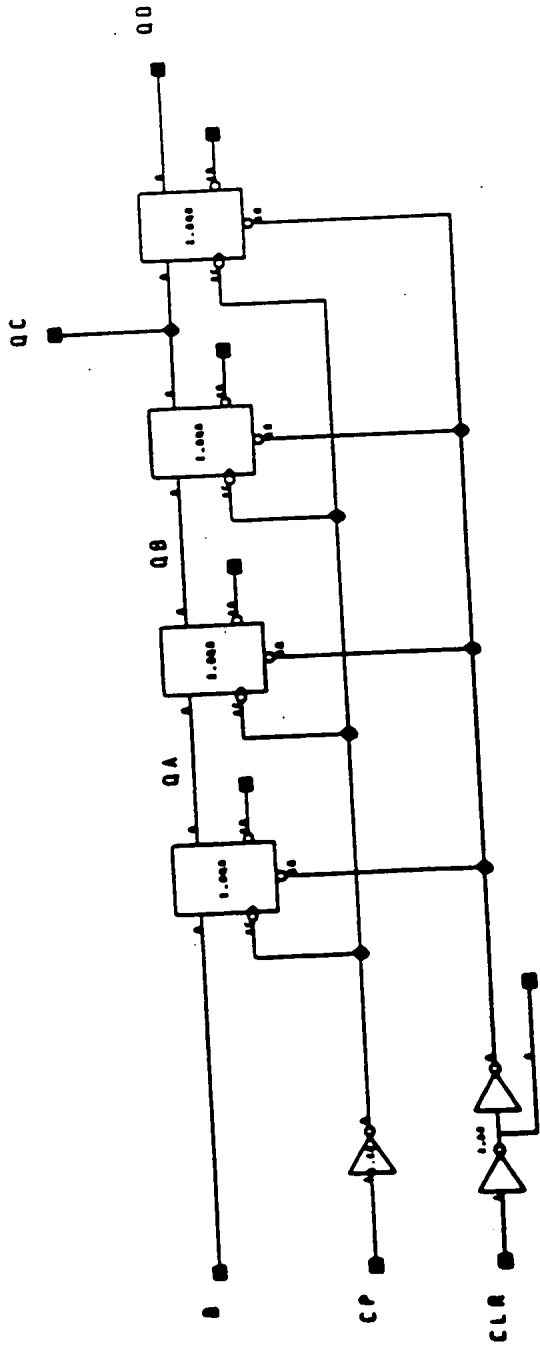


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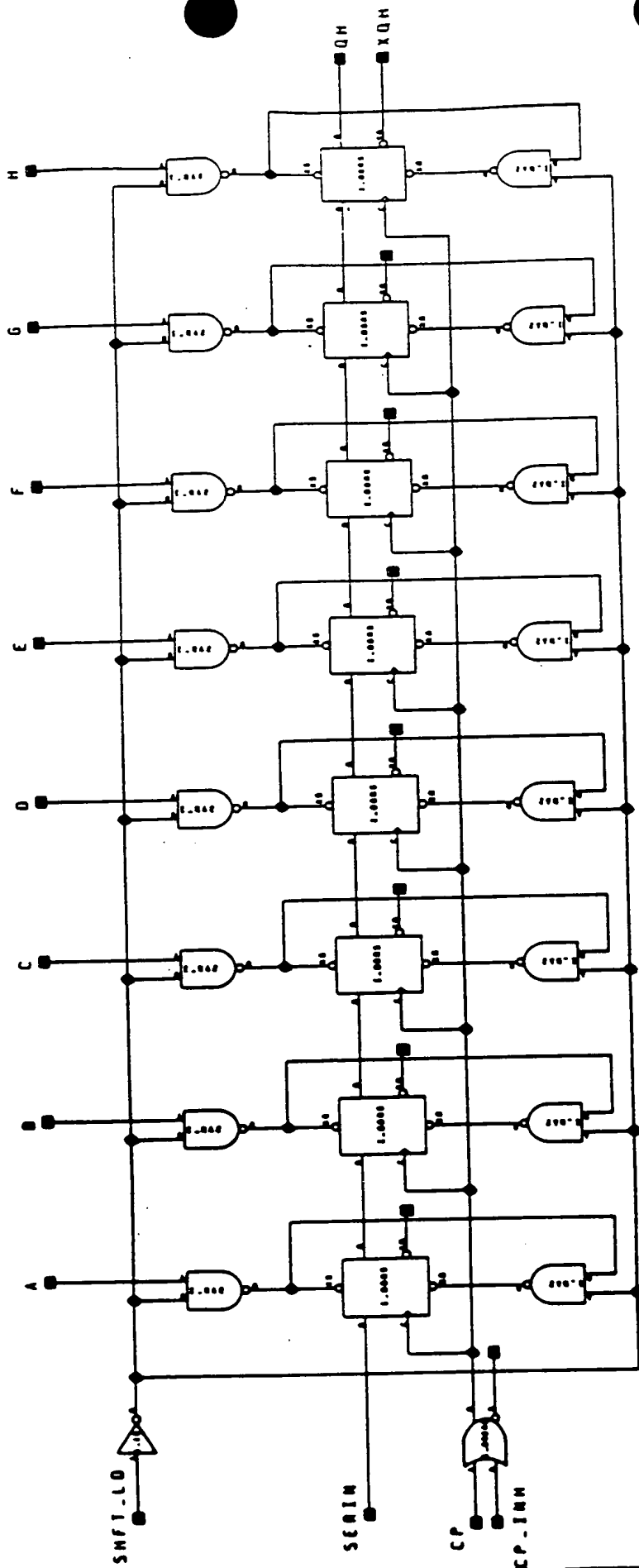
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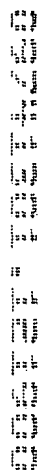
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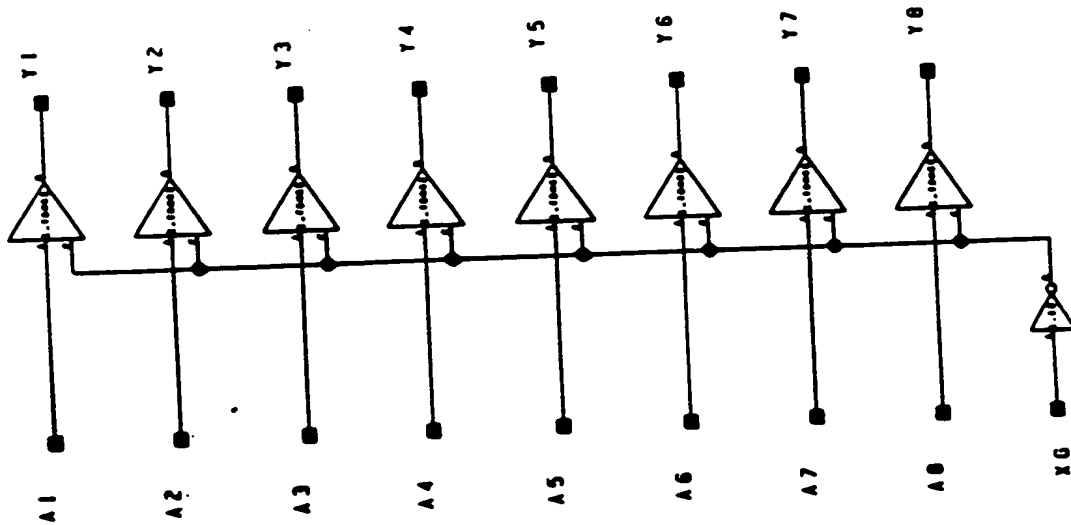
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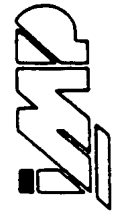
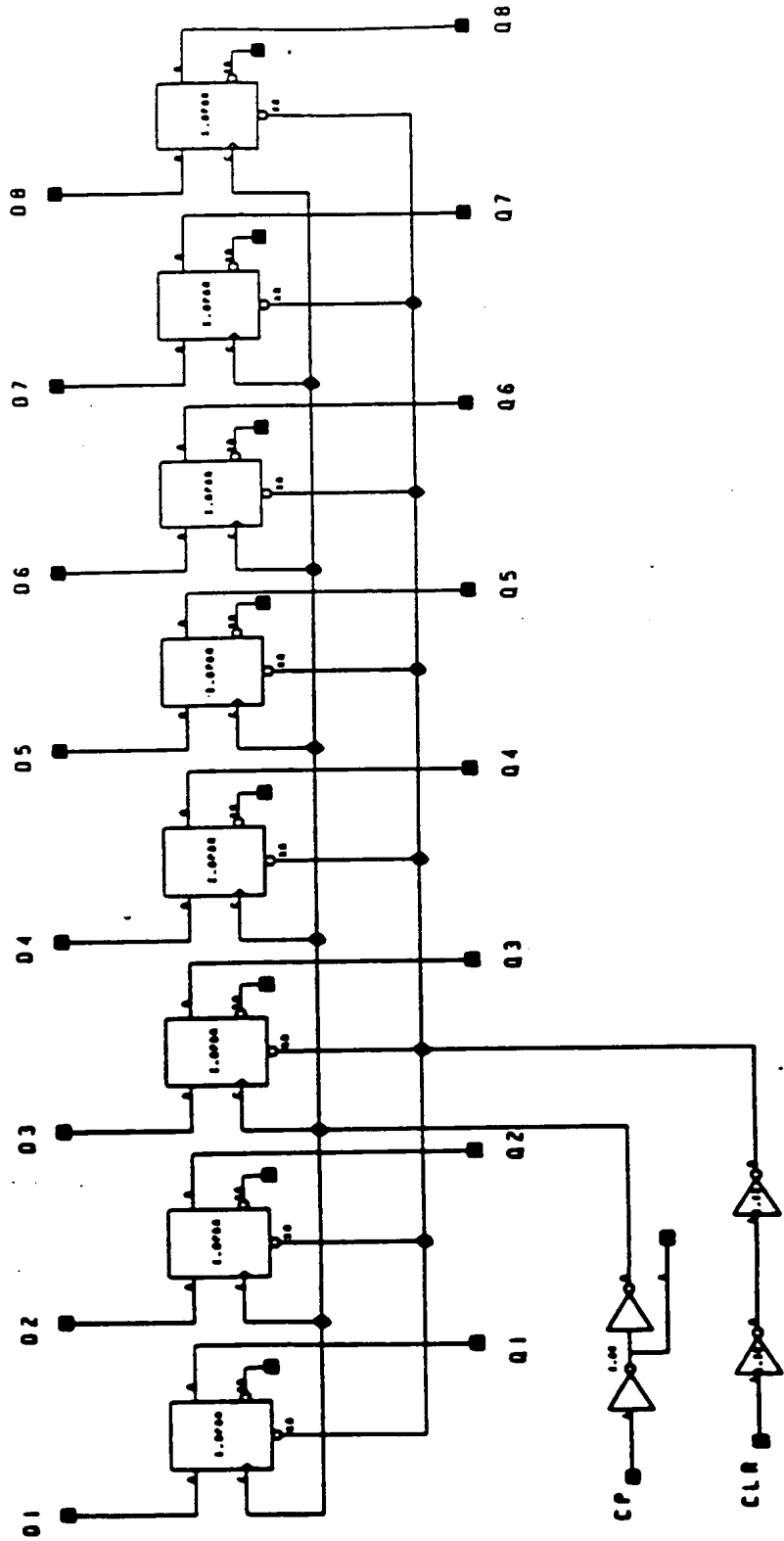




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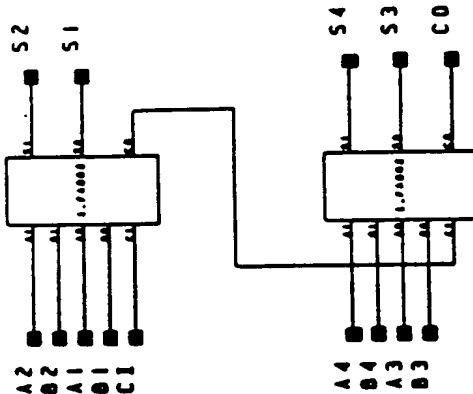


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 REVISION
 COMMENTS IMP0611-0

SCHEMATIC DBE 1200
 P/N 10-01-00003



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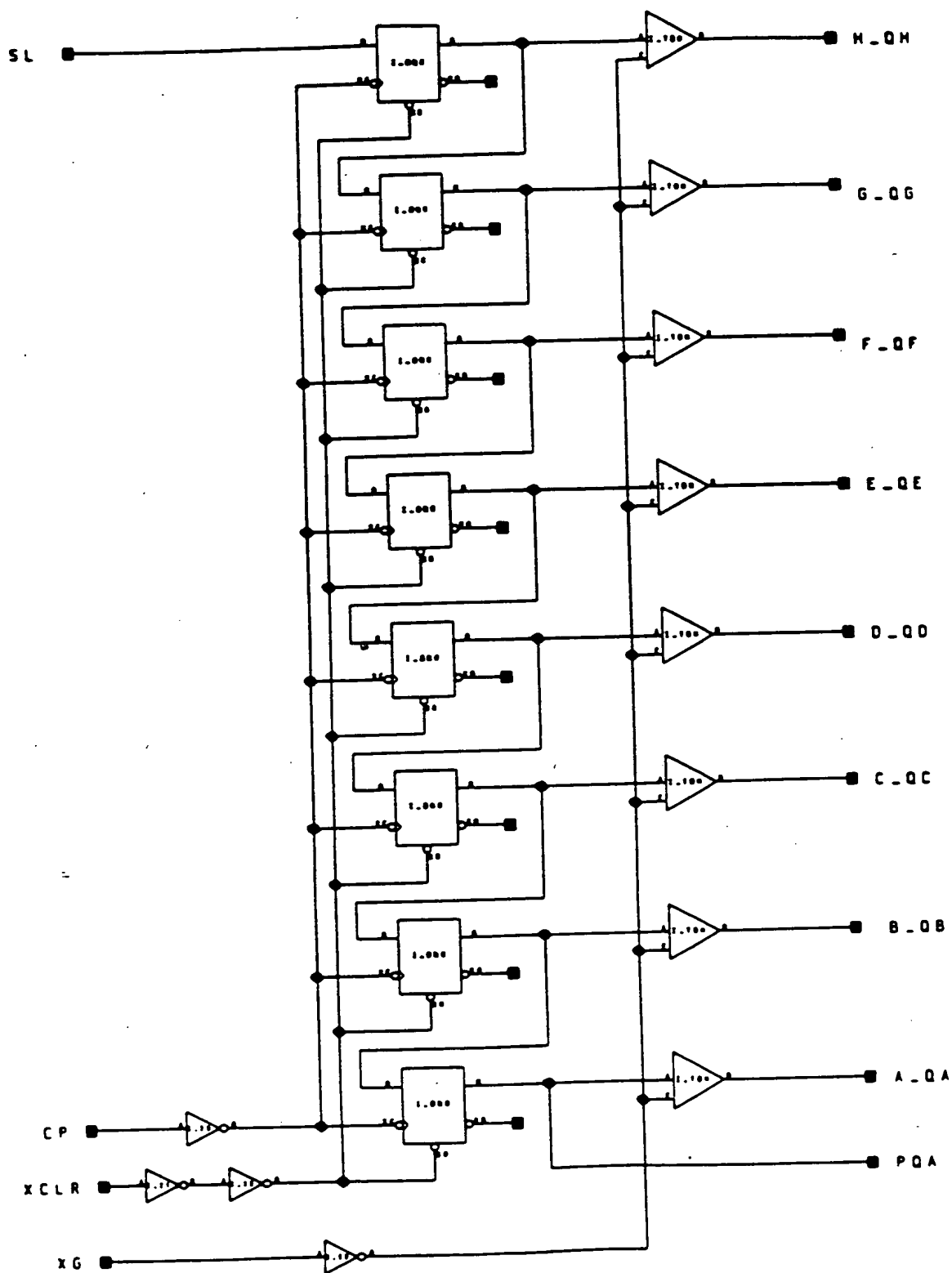
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IN SIGHT TELECAST

REVISION

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SCHEMATIC DBE 1200
P/N 10-01-00003

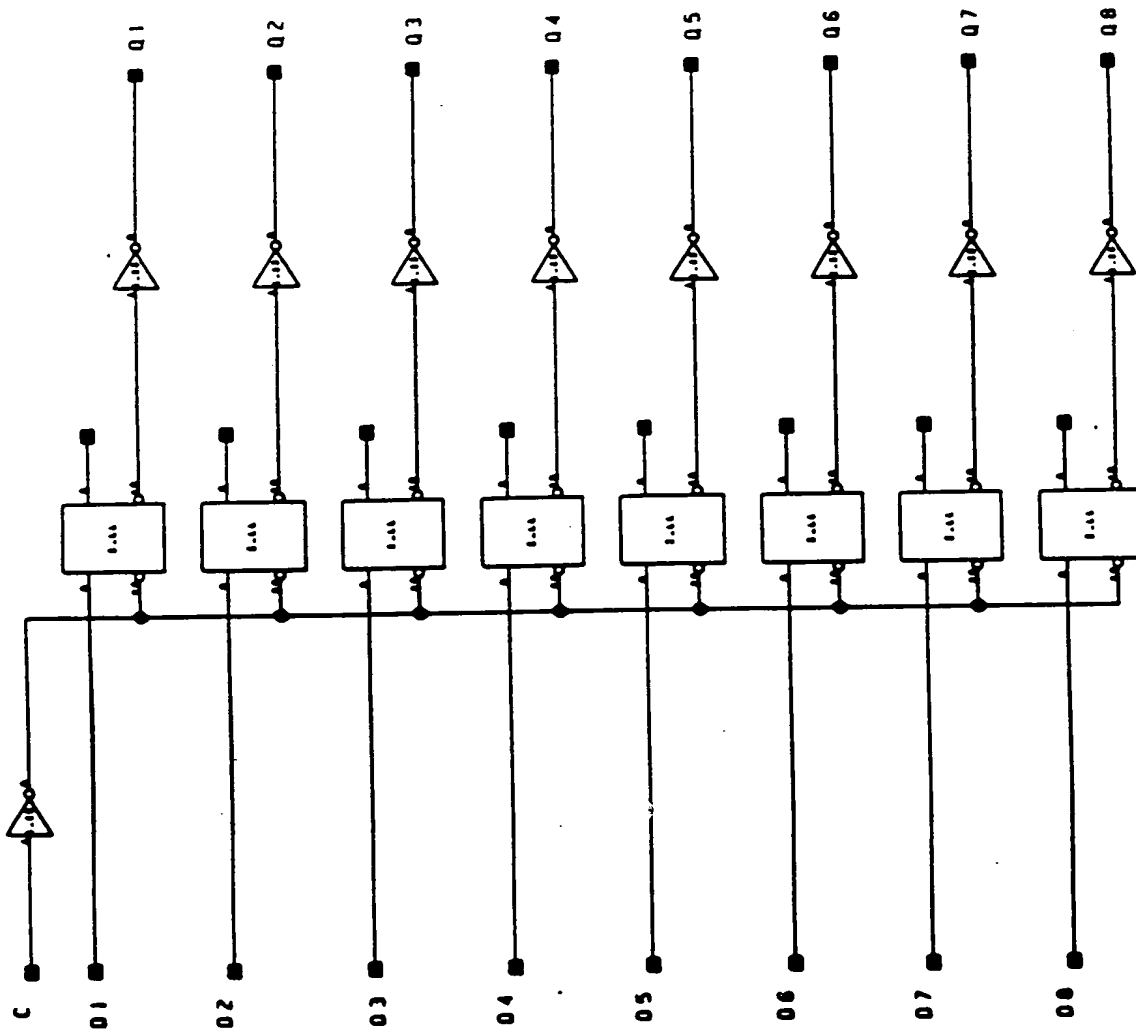


SCHEMATIC 7BE 1200
P/N 10-01-0003



TITLE M299-12
PAGE 54 OF 60
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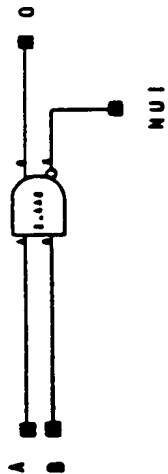


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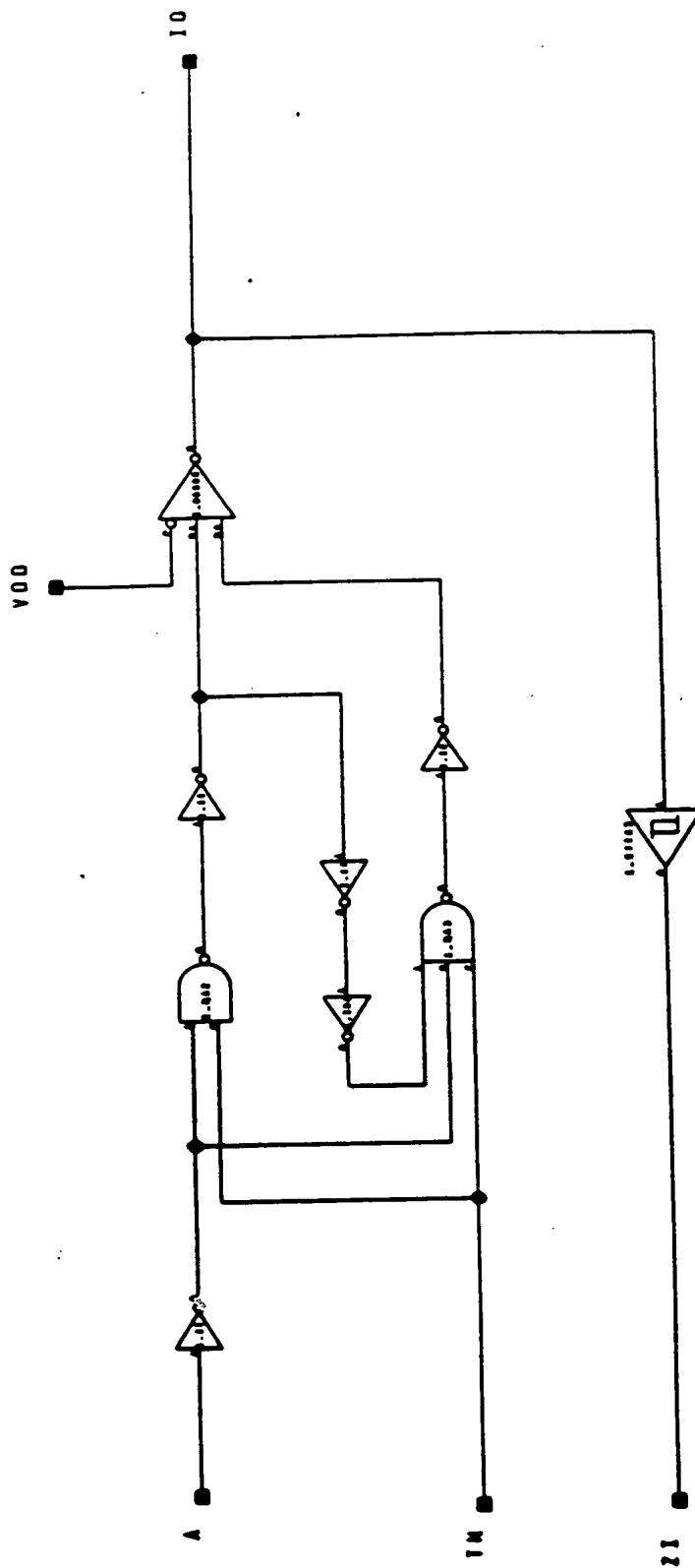
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TITLE AM02
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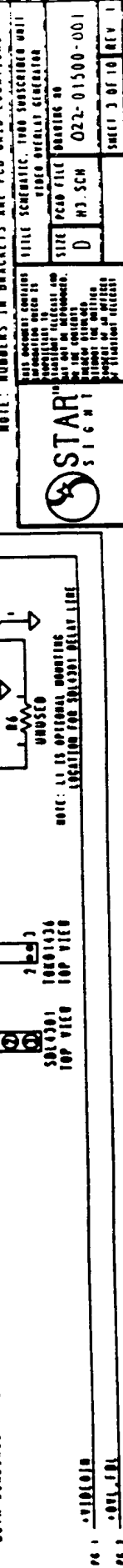
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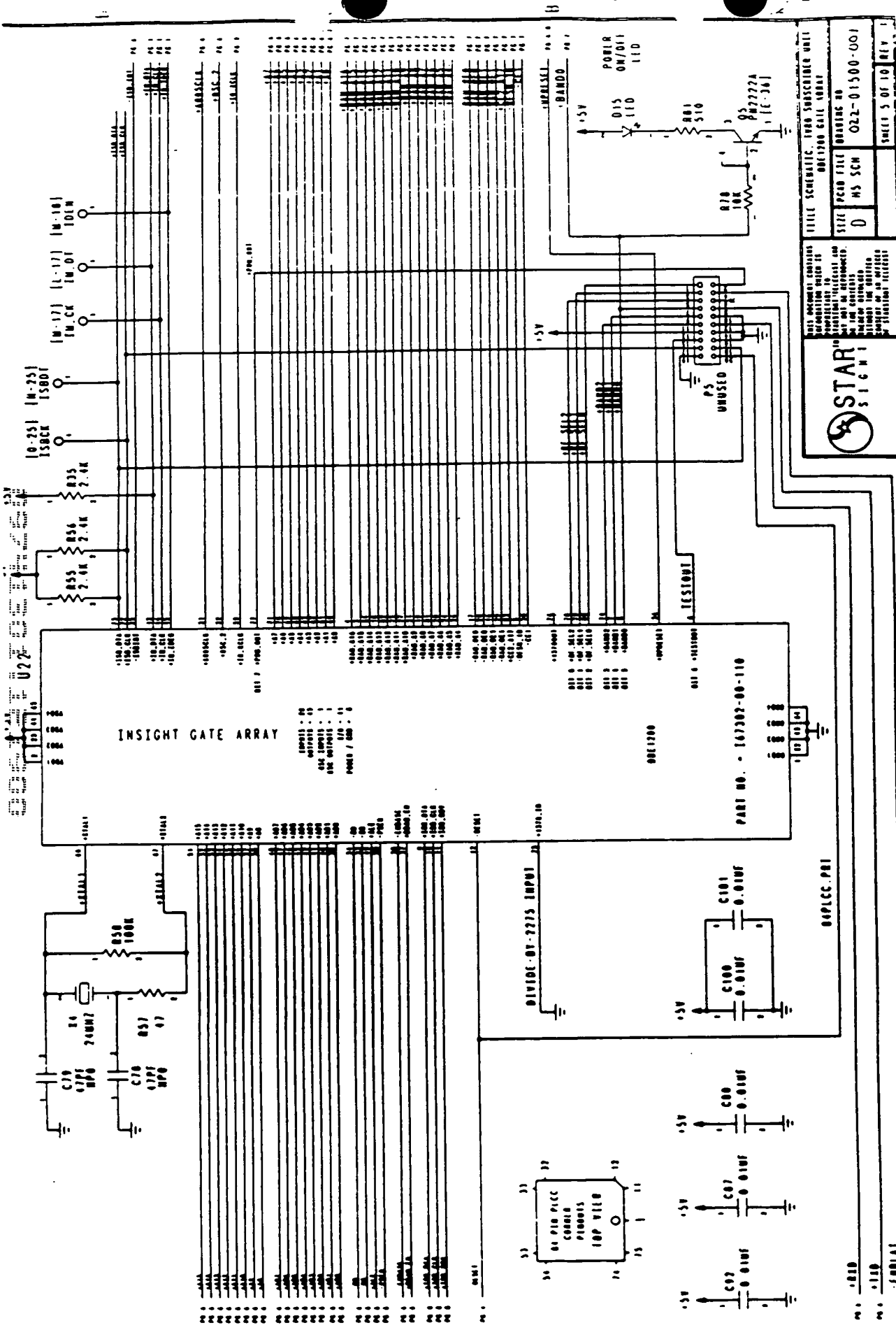
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SCHEMATIC DBE 1200
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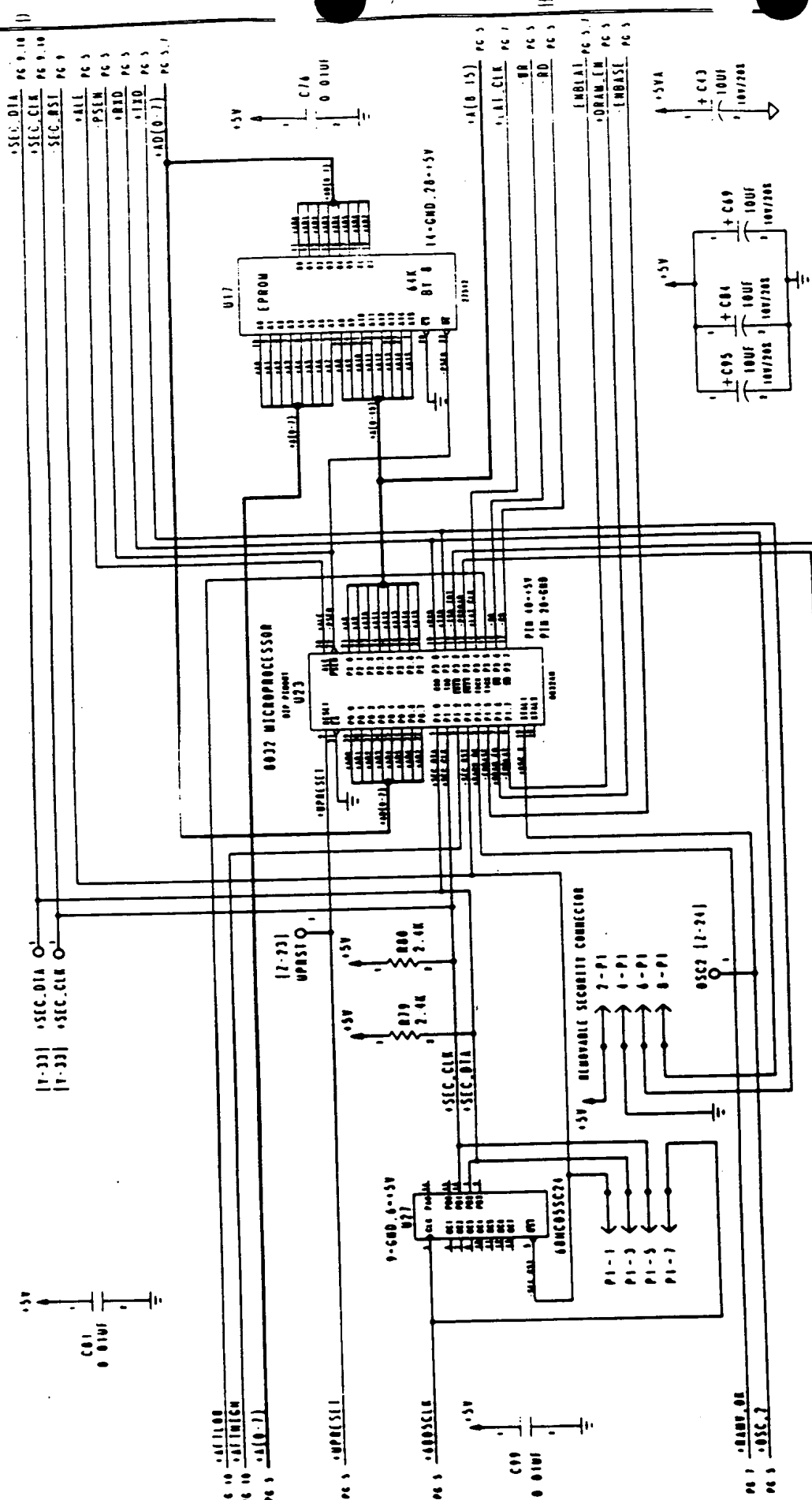


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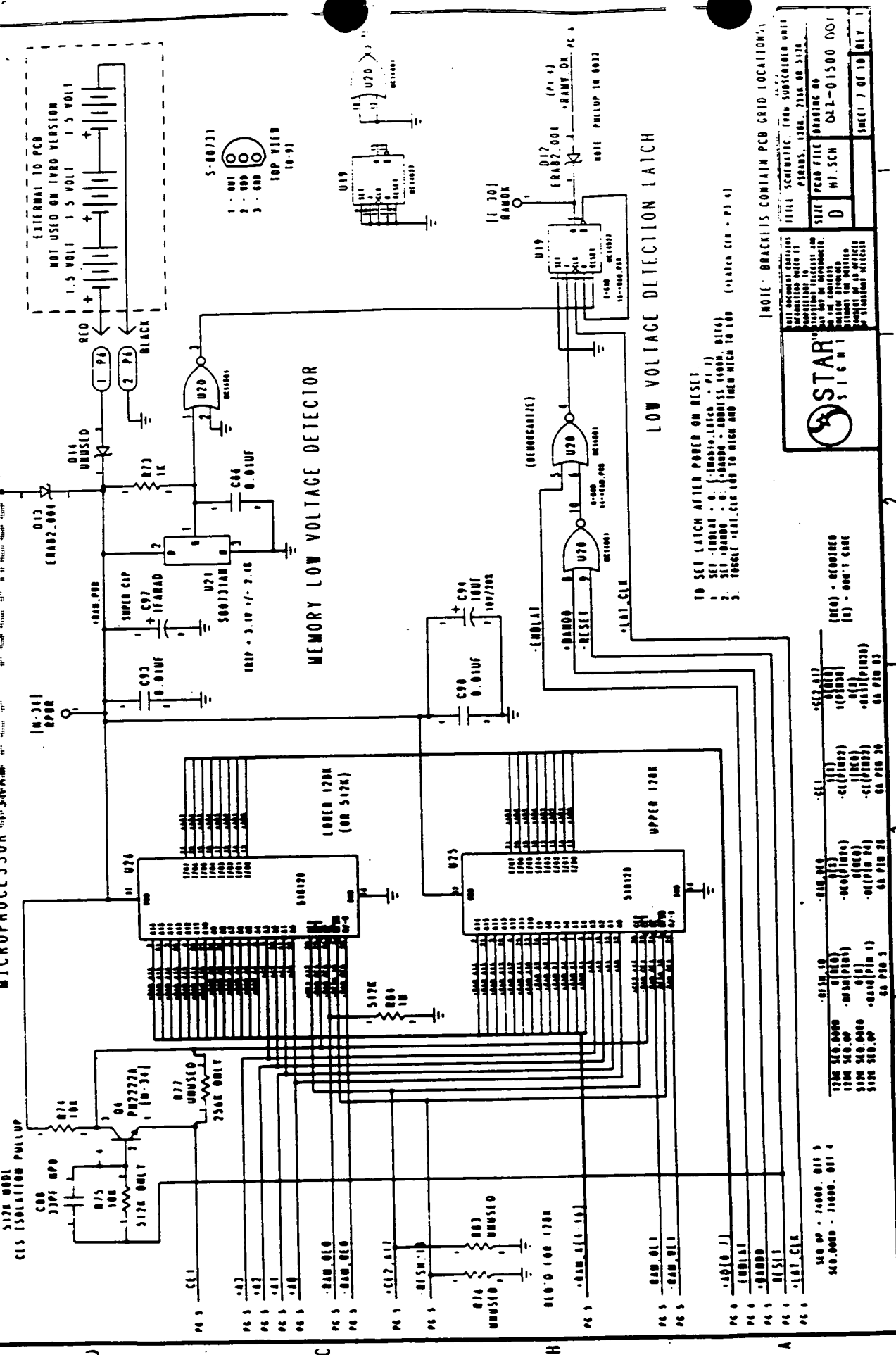
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 Tel: 619-591-1000 Fax: 619-591-1001
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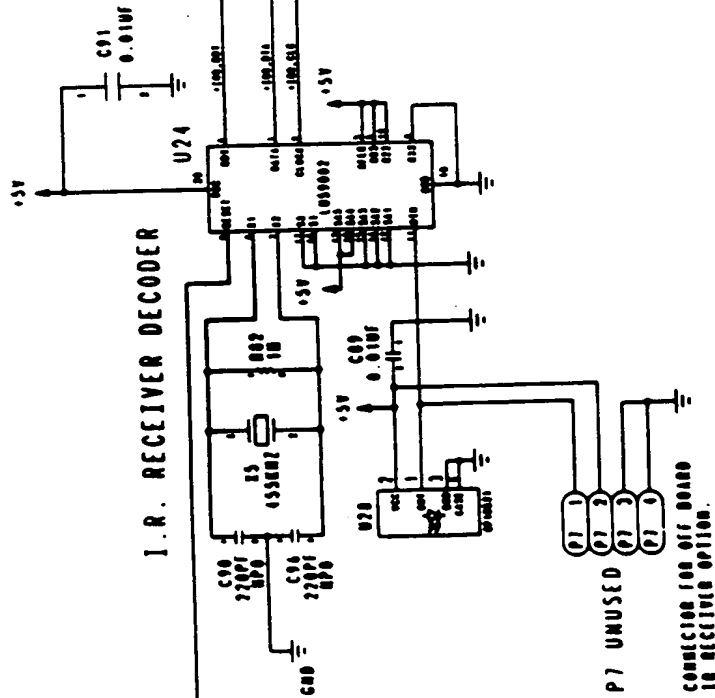
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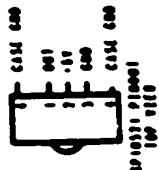
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I.R. RECEIVER DIODE MODULE



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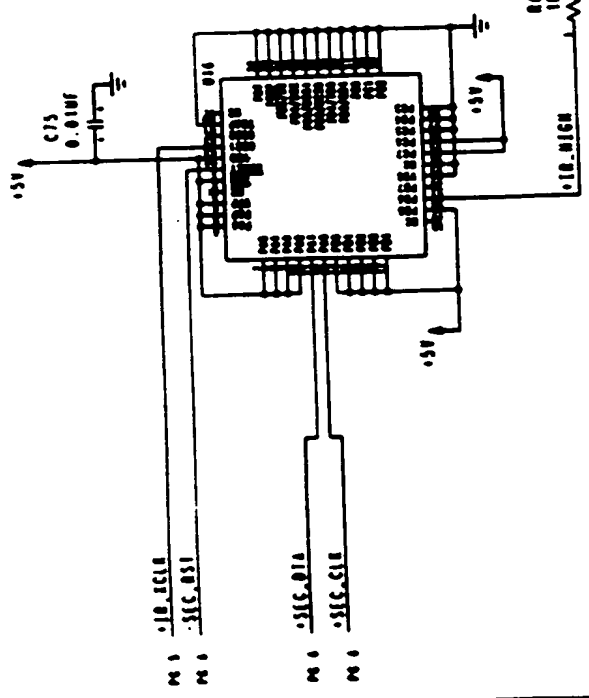
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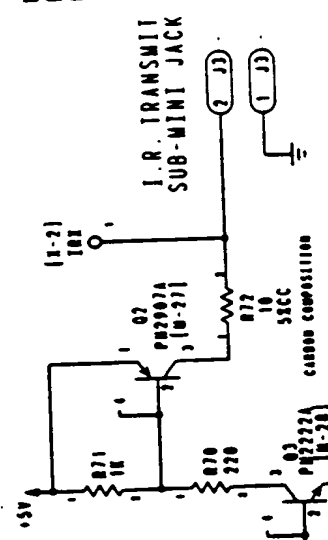
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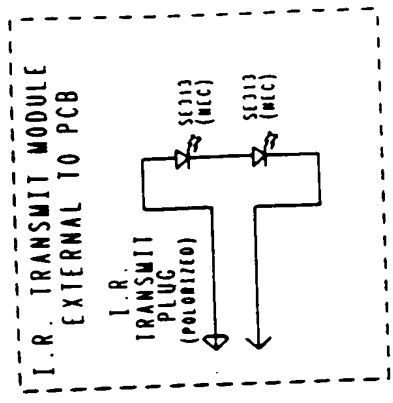
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UNIVERSAL I.R. REMOTE
TRANSMIT CHIP



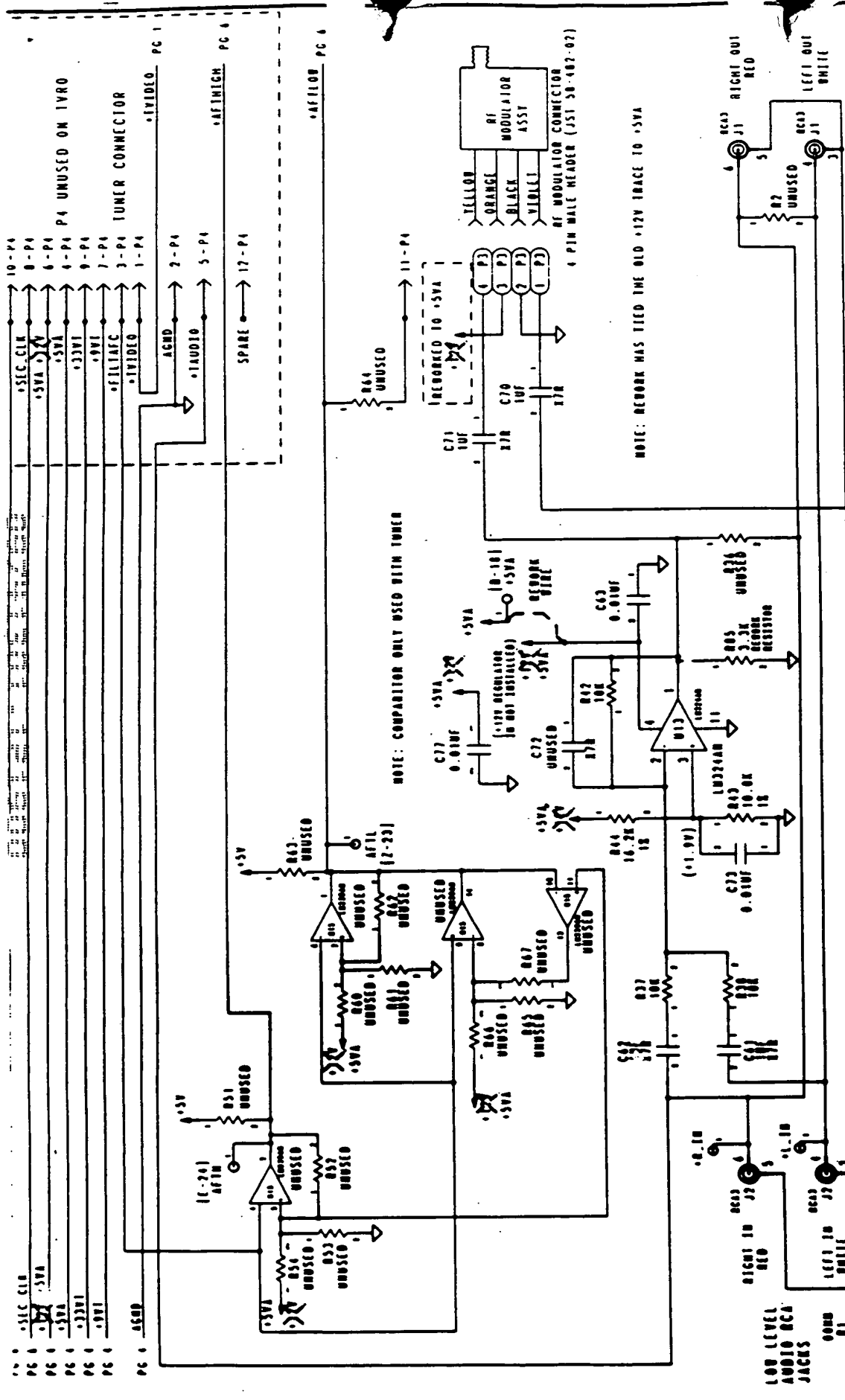
I.R. DRIVER



NOTE: ALTERNATE I.R. DIODE - STANLEY AN304

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